Course Outline Public

Edit Resource (/COMP3222/19T3/resources/31399/edit)

Contents

- Course Details
- Course Summary
- Course Timetable
- Course Aims
- Student Learning Outcomes
- Assumed Knowledge
- Teaching Rationale
- Teaching Strategies
- Assessment
- Supplementary Examinations
- Student Conduct and Plagiarism
- Course Schedule
- Lab Kits
- Resources for Students
- Course Evaluation and Development

Course Details

Course Code	COMP3222
Course Title	Digital Circuits & Systems
Units of Credit	6
Course Website	http://cse.unsw.edu.au/~cs3222 (http://cse.unsw.edu.au/~cs3222)
Handbook Entry	http://www.handbook.unsw.edu.au/undergraduate/courses/current/COMP3222.html (http://www.handbook.unsw.edu.au/undergraduate/courses/current/COMP3222.html)

Course Summary

This course teaches students the fundamentals of digital design.

The course introduces the components of digital systems, explains how these are described in the VHDL hardware description language and familiarizes the student with the implementation of digital circuits using FPGA prototyping boards.

Students are expected to attend lectures, tutorials and laboratories.

Students are assessed via the completion of lab exercises, a class test and final theoretical and practical exams.

Course Timetable

The course timetable is available here (/COMP3222/19T3/timetable) .

Course Aims

This course aims to provide students with a knowledge of problem solving with digital logic circuits & systems. The basic building blocks of combinational and sequential circuits are introduced to enable students to develop circuit solutions to problems and to understand the design and operation of hardware models of digital systems. Students will be introduced to the VHDL hardware description language as a means of describing circuits. Computer-aided design tools will be used to specify, simulate and implement a variety of simple digital systems. Students will learn how to implement and test their designs using Field-Programmmable Gate Arrays.

Student Learning Outcomes

After completing this course, students will be able to:

- design and implement combinational and synchronous sequential logic circuits,
- analyse combinational and sequential digital logic circuits,
- describe simple hardware functions using a hardware description language,
- make use of CAD tools to specify, simulate and synthesize circuit designs, and
- perform the steps involved in digital circuit implementation using Field-Programmable Gate Arrays.

This course contributes to the development of the following graduate capabilities:

Graduate Capability	**Acquired in**
scholarship: understanding of their discipline in its interdisciplinary context	theoretical and practical aspects of digital circuit design
scholarship: able to apply their knowledge and skills to solving problems	skills involved in the design and analysis of digital circuits
leadership: enterprising, innovative and creative	digital design using contemporary methods
professionalism: capable of independent, self-directed practice	theoretical and practical exercises

Assumed Knowledge

Before commencing this course, students should:

- be aware of the operation and programming of digital computer systems, and
- have a basic understanding of mathematical logic, discrete mathematical concepts, electrical circuit theory and electronics.

These are assumed to have been acquired in the courses ELEC1111, COMP2121, or equivalent courses and their prerequisites.

Teaching Rationale

An in-depth understanding of digital circuits and systems is fundamental to your ability to become a successful digital designer and computer engineer.

This course founds studies in computer architecture, embedded systems and configurable logic design, and also forms the basis for a deeper understanding of the hardware underpinnings of operating systems, compilers, networks, graphics systems, etc.

In this course we attempt to provide you with a rigorous and thorough grounding in the essential skills you will need to carry out digital design activities in the follow-on courses of Computer Architecture, Embedded Systems Design (Design Project A) and Configurable Systems Design (Design Project B).

To that end, we aim to provide a systematic, bottom-up coverage of the essential skills and theory, and to provide a top-down view of the broader area to motivate and highlight the interrelationship of the topics.

In order to learn, students are expected to play an active role in participating by continuously thinking about *HOW* what is said in lectures or read in texts or on the web relates to what you already know from this course and related studies. You are encouraged and expected to ask questions to clarify contradictions or uncertainties in your understanding as we proceed.

In this course, we also place considerable value on putting theory into practice through guided laboratory and tutorial exercises. Staying up-to-date with these will help enormously in picking up the skills we expect you to have acquired by the end of the course.

Teaching Strategies

This course will involve:

- Lectures, to introduce concepts, show examples
- Tutorials, to reinforce concepts and provide additional examples, and
- Lab Work, to introduce technology and put theory into practice.

The course will also involve:

- Theory Tests, to encourage study and more formally gauge uptake of theoretical skills, and a
- Practical Test, to encourage participation in laboratory work and to determine whether acquired skill levels are sufficient to pass the course.

Assessment

Assessment Item	**Contribution**
Labs	35%
Week 5 Class Test	15%
Final Theory Test	25%
Final Practical Test †	25%
TOTAL	**100%**

† Students must obtain more than 40% of the available marks in the Practical Test to pass the course.

Supplementary Examinations

A student will only be offered a supplementary examination (supp) if:

- they miss the Final Tests with a good medical excuse; the final mark is calculated in the same way as for students who sat the original tests, or
- they have obtained more than 45% of the total marks available AND more than 36% of the available marks in the Practical Test; these students can expect a maximum mark of 50 if they successfully

complete the supp.

There is no supp for the Class Test. Students who miss this test will have their final mark scaled to compensate for the missing test result if they provide a valid documented excuse.

Student Conduct and Plagiarism

The **Student Code of Conduct** (Information (https://student.unsw.edu.au/conduct) , Policy (https://www.gs.unsw.edu.au/policy/documents/studentcodepolicy.pdf)) sets out what the University expects from students as members of the UNSW community. As well as the learning, teaching and research environment, the University aims to provide an environment that enables students to achieve their full potential and to provide an experience consistent with the University's values and guiding principles. A condition of enrolment is that students *inform themselves* of the University's rules and policies affecting them, and conduct themselves accordingly.

In particular, students have the responsibility to observe standards of equity and respect in dealing with every member of the University community. This applies to all activities on UNSW premises and all external activities related to study and research. This includes behaviour in person as well as behaviour on social media, for example Facebook groups set up for the purpose of discussing UNSW courses or course work. Behaviour that is considered in breach of the Student Code Policy as discriminatory, sexually inappropriate, bullying, harassing, invading another’s privacy or causing any person to fear for their personal safety is serious misconduct and can lead to severe penalties, including suspension or exclusion from UNSW.

If you have any concerns, you may raise them with your lecturer, or approach the School Ethics Officer (mailto:ethics-officer@cse.unsw.edu.au) , Grievance Officer (mailto:grievance-officer@cse.unsw.edu.au) , or one of the student representatives.

Plagiarism is defined as (https://student.unsw.edu.au/plagiarism) using the words or ideas of others and presenting them as your own. UNSW and CSE treat plagiarism as academic misconduct, which means that it carries penalties as severe as being excluded from further study at UNSW. There are several on-line sources to help you understand what plagiarism is and how it is dealt with at UNSW:

- Plagiarism and Academic Integrity (https://student.unsw.edu.au/plagiarism)
- UNSW Plagiarism Procedure (https://www.gs.unsw.edu.au/policy/documents/plagiarismprocedure.pdf)

Make sure that you read and understand these. Ignorance is not accepted as an excuse for plagiarism. In particular, you are also responsible that your assignment files are not accessible by anyone but you by setting the correct permissions in your CSE directory and code repository, if using. Note also that plagiarism includes paying or asking another person to do a piece of work for you and then submitting it as your own work.

UNSW has an ongoing commitment to fostering a culture of learning informed by academic integrity. All UNSW staff and students have a responsibility to adhere to this principle of academic integrity. Plagiarism undermines academic integrity and is not tolerated at UNSW. Plagiarism at UNSW is defined as using the words or ideas of others and passing them off as your own.

If you haven't done so yet, please take the time to read the full text of

- UNSW's policy regarding academic honesty and plagiarism (https://student.unsw.edu.au/plagiarism)

The pages below describe the policies and procedures in more detail:

- Student Code Policy (https://www.gs.unsw.edu.au/policy/documents/studentcodepolicy.pdf)
- Student Misconduct Procedure (https://www.gs.unsw.edu.au/policy/documents/studentmisconductprocedures.pdf)
- Plagiarism Policy Statement (https://www.gs.unsw.edu.au/policy/documents/plagiarismpolicy.pdf)
- Plagiarism Procedure (https://www.gs.unsw.edu.au/policy/documents/plagiarismprocedure.pdf)

You should also read the following page which describes your rights and responsibilities in the CSE context:

- Essential Advice for CSE Students (https://www.engineering.unsw.edu.au/computer-science-engineering/about-us/organisational-structure/student-services/policies/essential-advice-for-cse-students)

Course Schedule

Week	Lecture	Text *	Lab
1	Introduction & simplifying circuits Optimizing logic functions	Ch 1 & 2 Ch 4.1-4.7 & 4.12	Intro
2	Number representation & arithmetic circuits	Ch 5.1-5.5	Lab 1
3	Combinational circuit blocks	Ch 6	Lab 2
4	Flip-flops, Registers & Counters	Ch 7.1-7.16	Lab 3
5	Synchronous sequential circuits **!!! CLASS TEST: 3 pm Wednesday 16 October !!!**	Ch 8.1-8.9	Lab 4
6	VHDL for synthesis VHDL for simulation		Lab 7
7	Digital system design	Ch 10	Lab 7
8	Digital system design	Ch 10	Lab 9
9	Blackjack player Implementation technology	 Ch 3	Lab 11
10	Course wrap-up		marking

* Chapter references are to the course textbook — see below.

Lab Kits

Lab kits will be handed out during your first lab session in Week 1. These kits are in short supply and expensive to replace. We therefore ask that you take care of your kit until the final practical exam, after which they will be collected from you.

Should you discontinue the course, please return your kit as soon as practical to avoid inconvenience to yourself and the School. We may seek to recover from you the replacement cost of lost or unreturned kits (currently USD 127). In addition, you may have your mark in the course held at 00FL and/or be blocked from further enrolment at UNSW until the kit is returned.

Resources for Students

Course textbook:

- Stephen Brown & Zvonko Vranesic, Fundamentals of Digital Logic with VHDL Design, 3ed, McGraw-Hill, 2009

Recently used texts (still quite useful):

- Katz and Borriello, Contemporary Logic Design (2nd edition)
- Mano and Kime, Logic and Computer Design Fundamentals (3rd Edition)

Other resources:

- Altera on-line User Guides and Data Sheets

Course Evaluation and Development

This course is evaluated each session using myExperience. Your feedback is highly appreciated.

In 2018, as in previous years, students found the course challenging and interesting. They particularly liked the close connection between theory and practice and enjoyed both the lectures and labs. This year, we aim to improve the lab experience and manage the transition to 3+.

Resource created 8 months ago (Monday 19 August 2019, 10:14:57 AM), last modified 7 months ago (Tuesday 17 September 2019, 03:59:10 PM).

Comments

(/COMP3222/19T3/forums/search?forum_choice=resource/31399)
(/COMP3222/19T3/forums/resource/31399)



Xiangyu Zeng (/users/z5137425) 7 months ago (Sat Oct 12 2019 03:32:26 GMT+1100 (Australian Eastern Daylight Time))

Hi, do we have any sample class test? If not, what types of questions will be asked in the class test, are they similar to the tutorial questions?

Thanks!

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Yiyan Yang (/users/z5183946) 7 months ago (Fri Oct 11 2019 16:48:05 GMT+1100 (Australian Eastern Daylight Time)), last modified 7 months ago (Fri Oct 11 2019 16:48:43 GMT+1100 (Australian Eastern Daylight Time))

Hello, I am confused about our next class test on Week 5. I found that we have a reminder like this:

!!! CLASS TEST: 3 **pm Wednesday 16 October !!!**

But I didn't find any announcements telling us the place, set allocation, etc.

And also I have no idea of the topics we are to be examined, do the topics containing the following line mean that it wouldn't be examined in the class test?

> It is recommended that you practice solving more problems of this style in preparation for the final exam.

Thank you.

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Jashank Jeremy (/users/z5017851) 7 months ago (Fri Oct 11 2019 17:16:55 GMT+1100 (Australian Eastern Daylight Time))

Oliver sent an announcement by email at around 2pm today which answers all these questions --- check your UNSW email.

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Yiyan Yang (/users/z5183946) 6 months ago (Sat Oct 12 2019 21:16:30 GMT+1100 (Australian Eastern Daylight Time))

Thank you

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