Course Outline

COMP4601
Design Project B
Teaching Period 1 2012

Contents

- Course Staff
- Course Details
- Course Summary
- Course Aims
- Student Learning Outcomes
- Assumed Knowledge
- Teaching Rationale
- Teaching Strategies
- Assessment
- Academic Honesty and Plagiarism
- Course Schedule
- Resources for Students
- Course Evaluation and Development

Course Staff

Contact Details

Staff Name	Role	Email	Phone
Oliver Diessel	Course Convenor	odiessel at cse.unsw.edu.au	57384

Consultations

Please email me to arrange a suitable meeting time.

Course Details

Course Code:	COMP4601
Course Title:	Design Project B
Units of Credit:	6
Course WebSite:	http://www.cse.unsw.edu.au/~cs4601/
Handbook Entry:	http://www.handbook.unsw.edu.au/undergraduate/courses/2012/COMP4601.html

Course Summary

COMP4601 is a team-based project development course involving the design and implementation of embedded and high performance or low power systems using FPGAs.

The 2012 project involves the development of an image processing system motivated by the requirements for a vision prosthesis. In outline, the project entails:

- converting a PAL video camera stream into RGB for display on a VGA monitor,
- processing the video stream to produce a rendering of the signals to be sent to an implanted device, and
- providing methods for enhancing various aspects of the video processing.

Project teams are expected to develop and report on their design, present their solution and report on their implementation and experiences.

See the 2012 COMP4601 Project Description for details.

Lectures will be provided in part to guide the design of the project solution and to provide the context within which configurable logic design is undertaken. Lectures will cover:

- motivations for hardware-based computer vision,
- video formats and video conversion,
- overview of FPGA-based systems and technology,
- FPGA-based computing techniques,

- implementing configurable systems, and
- case studies of configurable image processing systems.

Course Aims

This course aims to introduce students to the design and implementation of digital systems using configurable hardware. Students will work in teams to develop an FPGA-based solution to a compute intensive or low-power problem. The course aims to provide an understanding of the usefulness and limitations of custom logic as an alternative to processor-based computing.

Student Learning Outcomes

By the end of this course, students should be able to:

- design and implement digital systems using configurable logic,
- be able to distinguish when processor-based and logic-based solutions to computing problems are called for,
- explain and put into practice techniques for accelerating computation in hardware,
- independently study advanced techniques in digital design and implementation, and
- document and present their developments in a professional manner.

This course contributes to the development of the following graduate attributes:

Graduate Attribute	Where Acquired
an in-depth engagement with relevant disciplinary knowledge in its interdisciplinary context	Design & implementation tasks
the capacity for enterprise, initiative and creativity	Design & implementation tasks
the skills required for collaborative and multidisciplinary work	Team activities
the skills of effective communication	Presentation & documentation tasks

Assumed Knowledge

The formal pre-requisites for this course are COMP3211 and COMP3601.

It is assumed that, prior to taking this course, students:

- know how to program an FPGA,
- know how to construct combinational and sequential digital circuits,
- have the background to design architectural solutions to computational problems, and
- understand the concepts of sequential programming i.e. be familiar with the Von Neumann model of sequential computation and know an imperative programming language such as C.

Teaching Rationale

Hardware design and systems prototyping using field-programmable gate arrays is increasingly important and supports a booming embedded systems industry. In order to develop essential skills and to be industrially relevant, Computer Engineering students need to gain experience designing and implementing systems and components using programmable logic devices. The background necessary to understand the design problem and programmable logic technology will be outlined in classes. Students will work in teams to study and develop solutions using current methodologies to a computationally demanding and relevant problem.

Teaching Strategies

Formally, the course comprises two + one lecture hours and two lab hours per week. However, since this is a project course, there will be an emphasis on working informally with your team members to design solutions to the given problem.

Topics on configurable hardware and computing, and topics relevant to the project design, will be introduced and discussed during lectures. Attendance, particularly during the Tuesday lecture time, is expected.

Teams will develop a Design and Implementation Plan to be submitted during Week 5. The lecturer will provide feedback to each team on their plans.

The lecturer will meet with each group during their usual lab session in Week 7 to review progress on the project implementation. Attendance at these meetings by all team members is compulsory.

Teams will demonstrate the basic system requirement (video streaming) during Week 10. This is a major progress review. Attendance by all team members is required. Teams will have the opportunity to correct problems and re-demonstrate their system during Week 13 based on feedback given at this review.

A final demonstration of the "enhanced" system that provides video processing capabilities will be made during Week 13. Teams members will also undertake a reflective exercise and individually review their experiences with the lecturer this week.

Given the complexity of teaching a group of students with diverse backgrounds, the lecturer may revise or take detours from the plans stated in this Course Outline in order to better achieve the course objectives.

Assessment

Assessment in this course will be based on the following team-based components:

- Week 5 Design and Implementation Plan report (20%),
- Week 7 Progress Review (10%),
- Week 10 Demonstration of video streaming functionality (40%),
- Week 13 Demonstration of enhanced video processing (20%), and
- Week 13 Implementation report (10%).

Individual assessments will be moderated by the individual efforts made to contribute to team successes. All students will be required to complete a reflective questionnaire and to be debriefed during Week 13.

See the 2012 COMP4601 Project Description for details.

Academic Honesty and Plagiarism

Plagiarism is defined as "using the words or ideas of others and presenting them as your own". UNSW and CSE treat plagiarism as academic misconduct, which means that it carries penalties as severe as being excluded from further study at UNSW. There are several on-line sources to help you understand what plagiarism is and how it is dealt with at UNSW:

- Learning Centre: Plagiarism and Academic Integrity
- MyUNSW: Plagiarism and Academic Misconduct
- CSE: Addendum to UNSW Plagiarism Guidelines
- CSE: Yellow Form (whose terms you have agreed to)

Make sure that you read and understand these. Ignorance is not accepted as an excuse for plagiarism.

Course Schedule

An overview of topics that will be presented in lectures is provided during the Introductory lecture in Week 1.

See the 2012 COMP4601 Project Description for a timeline of tasks to be completed for the project.

Resources for Students

There is no prescribed text for this course. However, students will need to refer to product data sheets, standards, system documentation, reference books and technical articles to gain the background needed to design and implement the project system. Some of these references will be identified during the course.

Course Evaluation and Development

Student feedback on this course will be obtained via electronic survey (CATEI) at the end of session and will be used to make improvements to the course. Students are encouraged to provide informal feedback during the session and to let the lecturer in charge know of any problems as soon as they arise. Every reasonable effort to address concerns will be made.

In response to feedback received during 11s1, the following improvements have been made to the course during 12s1: To address the natural tendency to leave things to the last minute, and in order to provide feedback that groups can act upon to improve their implementations, groups will be required to prepare a detailed design and implementation plan for submission during Week 4 and to demonstrate the core functionality of a working video capture and presentation system during Week 10. Feedback will be provided to individual groups and will be able to resubmit their report and/or re-demonstrate their core system to improve their marks. Final demonstrations in Week 13 will evaluate the enhancement of the core system to provide the desired signal processing support for the technical interface of a bionic vision system.