ASEHA: A Web Services Protocol Modelling Formalism

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UNSW-CSE-TR-0521
November 2005

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Abstract

Agents require standard and reliable protocols to interact with different service providers in order to provide high quality service to customers over the web. Many useful protocols are coming into the market, but are ambiguously specified by protocol designers and without being fully verified. These can lead to interoperability problems among implementations of same protocol and high software maintenance costs.

In this paper, we propose a hierarchical automata-based framework to model the necessary features of protocols to verify their correctness. Our experience shows that the graphical models produced, provide invaluable insights and can be used to complement specifications to drastically reduce, if not eliminate, ambiguities. We illustrate our formalism with a version of \textit{WS-AtomicTransaction} protocol.
1 Introduction

Among their many goals, Web Services (WS) are designed to help agents to communicate, through a standard interface, with their partners regardless of platforms, thereby solving the interoperability problem.

Figure 1 illustrates a fictional travel arrangement facility, which is often used in the literature [10]. In this case, the agent and service providers all belong to the same company (Z) but are potentially in different locations. Customers wishing to go on a trip may want to book a ticket with Z Airlines, reserve a room with Z Hotel, and rent a car with Z Car Services. They may want the ticket only if the hotel and car are also available and the agent must find a way to make this possible.

A standard protocol that allows collaboration among these partners would be highly useful in this situation. Many such protocols have been defined over the past few years, the well known ones being WS-AtomicTransaction (WS-AT) [8], WS-BusinessActivity [9] and Business Transaction Protocol [4].

Unfortunately, the use of non-formal language in the specifications re-introduces the interoperability problem among the implementations of the same protocol, which is supposedly addressed by WS. This is because the non-formal language
could be, and often is, interpreted differently by different service providers and furthermore, not all possible interaction scenarios may have been fully considered in the specifications. Consequently, these can potentially lead to financial loss due to costs of software maintenance.

There have been research efforts towards modelling and automated verification[1, 3, 5, 6, 7, 16, 17, 20, 23], which work well for network protocols. Due to significant differences between network and WS protocols, these formalisms are not directly applicable to the latter. On the other hand, the recent formalisms for WS protocols [13, 14] have not shown any noticeable signs of adoption in the industry perhaps due to their complexity and intimidating syntax.

We present a formalism called Asynchronous Extended Hierarchical Automata (ASEHA) inspired by EHA[19] that is informed by the study of WS protocols. It has a sound mathematical foundation and aims to reconcile desirable features found in other formalisms while still maintaining syntactic and semantical simplicity.

We are using this formalism to model and verify commonly used WS protocols. Consequently, we find that the formalism provides invaluable insights and previously ‘unthinkable’ possible scenarios about the protocol in question. The modelling exercise also leads to concise, unambiguous and comprehensive documentation of the functional aspects of the protocols that can be used by both protocol designers and implementation teams.

1.1 Related Work and Overview

The area of protocol verification has been studied extensively over the past decades. The recent survey by Bhaduri and Ramesh [2] uncovers most of the recent advances. Most of the graphical models are based on statecharts[11] variants. While there are many different graphical models with different semantics, the verification is mainly handled by model checking using SMV[18] and SPIN[12].

Research in [1, 3, 5, 6, 7, 16, 17, 20, 23], including EHA[19], work well for network protocols but are not directly applicable to WS protocols. The main differences between network and WS protocols include heterogeneity of entities, communication mechanism and the complexity of the whole protocol interaction. Furthermore, some WS protocols operate using a two pipe model meaning that there is an application message exchange and a separate protocol message exchange. The application exchange introduce causal dependencies not apparent in the protocol message exchange. These will become apparent in the later sections of this paper.

However, limitations in the existing formalisms include concise graphical representation of message exchange permutations and reliance on broadcasting as the main means of communication. We have instead adopted the shared buffer model which closer to what actually happens during WS protocol interactions. It also
abstracts away unnecessary communication details while allowing analysis of correctness of the protocols. We have also integrated desirable features from existing formalisms such as state hierarchy, inter-level transitions (supported indirectly), concurrency and variable support[2, 22].

The models used in [13, 14] have yet to enjoy great success in the industry, most likely due to their syntactic and semantic complexity. It is believed that the primary resistance for adopting formal methods is that designers are unfamiliar with specification processes, notations and strategies. Our formalism is very simple and intuitive to understand and use. We believe that a graphical approach to WS protocol specification is more likely to be successful while abstracting away the mathematics behind it.

1.2 Paper Contributions

In this paper, we describe an automata based framework for modelling the essential components of WS protocols in order to assess their correctness. This framework can be used by designers to make sure that their protocols work as intended before release into the market. We claim that the graphical model produced together with some additional information, should be adequate for engineers to understand and implement the protocols. Furthermore, the framework can also be used by an ‘external reviewer’ to model newly released protocols and determine their correctness.

Correctness is determined by automatic verification—the model is translated into the input language of a suitable tool such as SPIN. Verification, however, is not focused here.

We are currently using this framework to generate automata based models for some popular WS protocols, which can complement their specification and drastically reduce their size.

The paper is organised as follows: Section 2 presents a complete informal overview of the proposed framework, Section 3 contains the formalization of ASEHA while introducing its different components in a bottom-up fashion. We give the semantics of ASEHA in Section 4 and discuss its features in Section 5. A case study is given in Section 6 and finally we conclude in Section 7 with some hints on future work.

2 Overview of ASEHA

We illustrate our framework by a simple version of WS-AT. In Figure 2, the WSAT automaton, called the root automaton, has one AND-state[11, 19] (w0), as indicated by the presence of dashed lines and three basic states namely co, ab and ex.
\(w_0\) is also the default state for \(WSAT\) as identified by an incoming arrow and \(co, ab\) and \(ex\) are its final basic states as indicated by the double borders. \(w_0\) is mapped to three automata: \(Participant\), \(Initiator\) (Figure 3) and \(Coordinator\) (Figure 4). A non-basic state is implicitly a final state, therefore, \(w_0\) is also a final state of \(WSAT\).

Each entity of the protocol has an ID associated with it and a set of variables and is modelled by a single automaton or a set of automata. In this case, the \(Participant\) and \(Initiator\) each represent one entity with IDs \(P\) and \(I\) respectively. The \(Coordinator\) automaton has three basic states (\(c_0, c_1, c_5\)) and has three OR-states[11, 19] (\(c_2, c_3, c_4\)) as indicated by the shade difference. The \(Registration\) automaton, which refines \(c_2\) is shown on the right in Figure 4. \(c_3\) and \(c_4\) in turn are refined by \(CommitTrans\) and \(AbortTrans\) (Figure 5) respectively. Together, \(Coordinator\), \(Registration\), \(CommitTrans\) and \(AbortTrans\) form an entity with ID \(C\) and have access to the variables \(p, v\) and \(d\).

At the beginning the default state of the root automaton(\(w_0\)) is active and consequently makes the states \(p_0, i_0\) and \(c_0\) active. In ASEHA, only one transition can be taken at a time and in an asynchronous fashion. Each transition label has a trigger, an action and a target determinator component. For a transition to be enabled, the trigger must evaluate to true and the action must be unblocked.

In the label of the transition, from state \(p_0\) to \(p_1\), \(in(i4)\) and \(Register, volatile!C\) are the trigger and the action components respectively. The transition can only be taken if state \(i_4\) in the \(Initiator\) automaton, is active (which is currently not the case).

Thus, the only possible transition, at this point, is \(CCC!C\), which causes the message \(CCC\) to be put in the shared buffer and can only be removed by an automaton belonging to the entity \(C\). After this transition is taken, \(i_0\) becomes inactive and \(i_1\) becomes active. The only next possible transition is \(CCC?I\) found in the \(Coordinator\) automaton. This causes the removal(reception) of the \(CCC\) message from the shared buffer by a get action. A get is blocking whereas a put is always non-blocking. This process continues until no more transitions are possible. Correctness is determined by checking if all active states are final states.

When \(c_2\) eventually becomes active, it also causes \(r_0\), the default state of \(Initiator\), to become active simultaneously. If there are more than one enabled transition, one is taken non-deterministically. Later, if the transition \(c_2\) to \(c_5\) is taken, \(c_2\) becomes inactive and so does the active state within the \(Registration\) automaton.

At some point, say the transition \(c_2\) to \(c_4\) is taken, \(c_4\) will become active. As indicated by the target determinator \({a_0}\), \(a_0\) will become active (since \(c_4\) is refined by \(AbortTrans\)) regardless of \(AbortTrans\)'s default state. However, in this case, the target determinator is redundant.
3 FORMAL DEFINITIONS

3.1 Definition of Sequential Automaton

Let $A$ be a sequential automaton, i.e. one in which at most one transition can be made at a time. $A$ is defined by the tuple $(\Sigma, \iota, \Lambda, \Phi, \rightarrow)$ where $\Sigma$ is the set of states, $\iota$ is the initial state, $\Lambda$ is the set of transition labels (which we will define precisely later), $\Phi$ is the set of final states and $\rightarrow \subseteq \Sigma \times \Lambda \times \Sigma$ is the transition relation. When $A$ is entered, the default state $\iota$ becomes active, unless specified otherwise by the incoming transitions’s target determinator (discussed in Section 3.6.1).

Example 1 For the sequential automaton $WSAT$, $\Sigma = \{w0, co, ab, ex\}$, $\iota = w0$, $\Phi = \Sigma$, $\rightarrow = \{(w0, \ell_1, co), (w0, \ell_2, ab), (w0, \ell_3, ex)\}$, where $\ell_1, \ell_2, \ell_3 \in \Lambda$.

A sequential automaton can have its states mapped to one or more automata to form a hierarchy. This mapping is specified by the refinement function defined next.
3.2 Definition of Refinement Function

For a set of sequential automata $\mathcal{F} = \{A_1, ..., A_n\}$ with mutually disjoint state spaces, $\Sigma_\mathcal{F} = \bigcup_{A \in \mathcal{F}} \Sigma_A$ is the union of all states of all automata in $\mathcal{F}$. The refinement function $\gamma : \Sigma_\mathcal{F} \rightarrow 2^\mathcal{F}$ maps a state $s$ of a sequential automaton to a subset of automata $G \subseteq \mathcal{F}$; $s$ is said to be refined by $G$ in such a case.

For any $s \in \Sigma_\mathcal{F}$, if $|\gamma(s)| = 0$, $s$ is a basic state, else if $|\gamma(s)| = 1$, $s$ is an OR-state else for $|\gamma(s)| > 1$, $s$ is an AND-state. Moreover, if $\gamma(s) = \{A'_1, ..., A'_n\}$, we say that $s$ is non-basic, $A'_1, .., A'_n$ are its sub-automata, they refine $s$ and $s$ is their parent state. If $s \in \Sigma_A$, $A$ is the parent automaton of $A'_1, .., A'_n$.

Analogous to [19], for $\gamma$ to be a valid function, the following conditions must hold:

- $\mathcal{F}$ can only have one root automaton (denoted by $\gamma_{\text{root}}$), i.e. $\exists A \in \mathcal{F} : A \notin \text{range}(\gamma)$. $\gamma_{\text{root}}$ is the top-level automaton, which does not have a parent state. It glues the entities together.
Each automaton, except from the root $\gamma_{root}$, has exactly one parent state i.e. $\text{range}(\gamma) = F \setminus \{\gamma_{root}\} \land \forall A \in F \setminus \{\gamma_{root}\} \exists s, s' \in \Sigma_F, A \in \gamma(s) \land A \in \gamma(s') \Rightarrow s = s'$.

All non-basic states of the automata of $F$ are final states, i.e. $\forall A \in F : (s \in A \land \gamma(s) \neq \phi) \Rightarrow s \in \Phi_A$.

These conditions imply that $\gamma$ does not contain any cycle.

**Example 2** The refinement function on $F = \{WSAT, Participant, Initiator, Coordinator, Registration, CommitTrans, AbortTrans\}$ with $\gamma_{root} = WSAT$ is:

$$\gamma = \{(w0, \{Coordinator, Participant, Initiator\}), (c2, \{Registration\}), (c3, \{CommitTrans\}), (c4, \{AbortTrans\})\} \cup \{(s, \phi) | s \in \Sigma_F \setminus \{w0, c2, c3, c4\}\}$$

$\gamma$ induces a successor function $\theta : F \rightarrow 2^F$. It returns all the automata whose parent automaton is $A$:

$$\theta(A) = \{A' | \exists s \in \Sigma_A : A' \in \gamma(s)\}$$
The irreflexive and reflexive transitive closures of $\theta$ are denoted by $\theta^i$ and $\theta^r$ respectively, i.e. $\forall A \in \mathcal{F}, A \subseteq \mathcal{F} : (A, A) \in \theta^i \Rightarrow A \notin A$ and $\forall A \in \mathcal{F}, \exists A \subseteq \mathcal{F} : (A, A) \in \theta^r \wedge A \in A$.

Example 3 Using the definition of the refinement function in Example 2, we have:

$\theta = \{(\text{WSAT}, \{\text{Participant, Initiator, Coordinator}\}), (\text{Coordinator}, \{\text{Registration, CommitTrans, AbortTrans}\})\} \cup \{(A, \phi) | A \in \mathcal{F} \setminus \{\text{WSAT, Coordinator}\}\}$

$\theta^i = \{(\text{WSAT}, \mathcal{F} \setminus \{\text{WSAT}\}), (\text{Coordinator}, \{\text{Registration, CommitTrans, AbortTrans}\})\} \cup \{(A, \phi) | A \in \mathcal{F} \setminus \{\text{WSAT}, \text{Coordinator}\}\}$

$\theta^r = \{(\text{WSAT}, \mathcal{F}), (\text{Coordinator}, \{\text{Coordinator, Registration, CommitTrans, AbortTrans}\})\} \cup \{(A, \{A\}) | A \in \mathcal{F} \setminus \{\text{WSAT, Coordinator}\}\}$
3.3 Entity Model

An entity that participates in a protocol is modelled by a sequential automaton or by a set of automata. Each entity has an ID and a set of variables associated with it. An ID can be thought of as a shorter form of the entity’s name for keeping the labels of transitions short. The mapping of an automaton to its entity ID is specified by the \( \text{EID lookup function} \).

Entities communicate with each other by exchanging messages through the shared buffer using put and get actions, as shown in Figure 6. Furthermore, only entities in ASEHA can access the buffer i.e. ASEHA is a closed system. To cope with the complexity of dealing with many entities, variables are of great help. They are mainly used for remembering certain responses of a group of entities and to handle message exchange permutations. The \( \text{evaluation function} \) keeps track of variables and their values.

3.3.1 Definition of EID Lookup Function

For a set of entity IDs \( \mathcal{D} = \{d_1, \ldots, d_m\} \), where \( m \leq |\mathcal{F}| \), an EID lookup function, \( \eta : \mathcal{F} \rightarrow \mathcal{D} \), maps an automaton to its entity ID. The \( \eta \) function indicates the entity that an automaton is part of—the root automaton is strictly not part of any entity as it represents the whole protocol interaction, but is given an entity ID to simplify the model. \( \eta \) is valid iff:

- All automata that refine the states of the root automaton \( \gamma_{\text{root}} \) have different EIDs, i.e. \( \forall A, A' \in \theta(\gamma_{\text{root}}) : (\eta(A) = d \land \eta(A') = d) \Rightarrow A = A' \), where \( d \in \mathcal{D} \).

![Figure 6: Interaction of entities](image-url)
All automata except the root $\gamma_{root}$ and the automata which refine its states, have the same EID as their parent automaton, i.e. $\forall A \in \theta(\gamma_{root}), A' \in \theta'(A) \Rightarrow \eta(A) = \eta(A')$.

Hereafter, we will refer to $\eta$ as ‘an EID function’.

### 3.3.2 Definition of Evaluation Function

Each entity with ID $d$, has a set of variables $\mathcal{V}_d$ associated with it. A variable can only hold EIDs for values. An evaluation function $\xi : \mathcal{V} \rightarrow D \times 2^D$, returns the current value and the set of buffered (remembered) values of a variable. A variable $v \in \mathcal{V}$ is initialized to $(d, \phi)$ unless specified otherwise. We define selector functions as follows: if $\xi(v) = (d, R)$, then $val(v) = d$ and $buffer(v) = R$.

**Example 4** Continuing from Example 2, for $D = \{W, P, I, C\}$, we have $\eta = \{(\text{WSAT, W}), (\text{Coordinator, C}), (\text{Registration, C}), (\text{CommitTrans, C}), (\text{AbortTrans, C}), (\text{Participant, P}), (\text{Initiator, I})\}$, and each entity’s set of variables is given by: $\mathcal{V}_W = \phi, \mathcal{V}_C = \{p, v, d\}$, $\mathcal{V}_P = \phi$ and $\mathcal{V}_I = \phi$.

This means that the Coordinator, Registration, CommitTrans and AbortTrans automata belong to the entity with ID = C and only these automata have access to the variables p, v, and d. Also, WSAT, Participant and Initiator belong to the entity with ID = W, ID = P and ID = I respectively but have no variables.

### 3.4 Definition of ASEHA

An ASEHA $\mathcal{H}$ is defined by $\mathcal{H} = (\mathcal{F}, \mu, \mathcal{V}, \gamma, \eta)$, where $\mathcal{F} = \{A_1, ..., A_n\}$ is a set of sequential automata with mutually disjoint state spaces, $\mu$ is a set of messages, $\mathcal{V} = \biguplus_{d \in D} \mathcal{V}_d$ is the set of variables (disjoint union of the variables of all entities), $\gamma$ is a refinement function on $\mathcal{F}$, and $\eta$ is the EID function on $(\mathcal{F}, D, \gamma)$, where $D$ is a set of EIDs.

Furthermore, $B \subseteq D \times \mu \times D$ represents the contents of the shared buffer used for communication among the entities. Intuitively, it stores messages that have been sent but not yet received.

Moreover, the visited list $T \subseteq \bigcup_{A \in \mathcal{F}} \Sigma_A$, stores the list of all states visited so far. A state is $s$ said to be visited when it becomes active. In section 4.2, we will explain how and when $T$ is updated.

We next formalize the notion of configuration before defining a transition label in ASEHA.
3.5 Definition of Configuration

A configuration is a set of states in \( \mathcal{H} \) that can be active simultaneously. Analogous to [19], a set \( C \subseteq \bigcup_{A \in \mathcal{F}} \Sigma_A \) is a configuration iff:

- Any automaton contributes at most one state to the configuration i.e. \( A \in \mathcal{F} \Rightarrow |\Sigma_A \cap C| \leq 1 \)
- Downward closure: if a state \( s \) is in \( C \) then its descendants (states lower that itself in the hierarchy) are in \( C \), i.e. \( s \in C \land A \in \gamma(s) \Rightarrow \exists s' \in \Sigma_A, s' \in C \).
- Upward closure: if a state \( s \) is in \( C \), then its ancestors (states higher that itself in the hierarchy) are in \( C \), i.e. \( \{s\} = \Sigma_A \cap C \land (\exists s' \in \Sigma_{A'} : A \in \gamma(s')) \Rightarrow s' \in C \).

The set of all configurations is given by \( Conf(\gamma) \) and the initial configuration \( C_{\gamma\text{root}} \) is the set containing only default states, i.e. \( \{C \mid C \in Conf(\gamma) \land \forall s \in C \exists A \in \mathcal{F} : s = \iota_A \} \)

**Example 5** If we define \( \mathcal{H} \) using the information from the previous examples, we have the initial configuration \( C_{\gamma\text{root}} = \{w0, p0, i0, c0\} \).

3.6 Definition of Transition Label

Interlevel transitions[11, 19] are not allowed but are simulated using triggers and target determinators. The motivations behind disallowing interlevel transitions are mainly that they introduce a lot of semantic complexity and make compositionality very hard, if not impossible[19].

Recall that an automaton \( A \in \mathcal{F} \) is defined by the tuple \( (\Sigma_A, \iota_A, \Lambda_A, \Phi_A, \rightarrow_A) \) and \( \rightarrow_A = \Sigma_A \times \Lambda_A \times \Sigma_A \). A label \( \ell \in \Lambda_A \) of a transition can be further defined as a 3-tuple \( (tr, ac, td) \), where \( tr \) is the trigger, \( ac \) is the action taken and \( td \subseteq \Sigma_{\mathcal{F}} \) is the target determinator, where \( \Sigma_{\mathcal{F}} = \bigcup_{A' \in \mathcal{F}} \Sigma_{A'} \).

The syntax of the labels used in the examples is:

\[
\text{trigger} : \text{action}\{\text{target determinator}\}
\]

If the trigger and the colon(;) are omitted, the trigger is assumed to be true, if the action is omitted, it is assumed to be \( \tau \) and if the target determinator, enclosed in braces (\( \{\} \)), is omitted, it is assumed to be empty.
3.6.1 Target Determinator

For the transition \((s, (tr, ac, td), s') \in \rightarrow_A\), if \(s'\) is a non-basic state, the default states of the sub-automata are recursively entered unless the target determinator \(td\) has been specified.

If \(C\) is the current configuration then, \(td\) is valid iff

\[
\text{head} = \left( C \setminus \left( \bigcup_{A' \in \Theta(A)} \Sigma_{A'} \right) \right)
\]

where

\[
\text{head} = \left( C \setminus \left( \bigcup_{A' \in \Theta(A)} \Sigma_{A'} \right) \right).
\]

Intuitively, if \(td\) is specified (non-empty), it has to specify the set of states to enter such that the resulting set \((\text{head} \cup \{s'\} \cup td)\) is a valid configuration. \((\bigcup_{A' \in \Theta(A)} \Sigma_{A'})\) is the set of states that will be exited and \(\{s'\} \cup td\) is the new set of states that will be entered. Although \(td\) can possibly be specified by fewer states, we stick to the above definition since it simplifies the semantics.

**Example 6** Consider the transition between \(c2\) and \(c4\) in the Coordinator automaton in Figure 4. If the current configuration is \(C = \{w0, p4, i6, c2, r2\}\), then if the transition is taken, \(\{c2, r2\}\) will be exited, \(\{c4, a0\}\) will be entered while \(\{w0, p4, i6\}\) will still be in the new configuration i.e. \(C' = \{w0, p4, i6, c4, a0\}\). In this case, the target determinator \(td = \{a0\}\) could have been omitted since the default state of \(\text{AbortTrans}\) is \(a0\).

3.6.2 Action

As mentioned earlier, an action \(ac\) can be a put, get or internal. There are two versions of the get and put actions—one based on a specific entity ID and the other based on the evaluation of the local operation sequence (LOS) function, which we define later.

Given the current value of the message buffer \(B\) is \(B\) and the following actions appear in a transition \(t \in \rightarrow_A:\)

\(\text{put}(msg, r)\) Puts the message \(msg \in \mu\) for the entity with ID \(r \in D\). Effectively, after the put action, the new value of \(B\) will be \(B \cup \{(\eta(A), msg, r)\}\).

\(\text{put}(msg, f_t)\) Similar to the function above, except that the tuple containing the message \(msg \in \mu\) is put based on the LOS function \(f_t\) to be explained in the next section.

\(\text{get}(msg, d)\) Returns the retrieved tuple from the message buffer \(B\). Effectively, a get action finds the tuple \((d, msg, \eta(A))\) in \(B\), where \(d \in D\) is the sender’s EID, \(msg \in \mu\) is the message and \(\eta(A)\) is \(A\)’s EID. If such a tuple is
found, the receive action in $A$ is unblocked else the receive action is blocked until such a tuple is found. When the transition is taken, the tuple is removed from $B$. After the receive action, the new value of $B$ becomes $B \setminus \{(d, msg, \eta(A))\}$.

$\text{get}(msg, f_t)$ Similar to the function above, except that the tuple containing the message $msg \in \mu$ is retrieved based on the LOS function $f_t$ to be explained in the next section.

$\tau$ is an internal action, i.e. one which is not visible to the environment.

Syntactically, an action may be specified in BNF as:

$$
\begin{align*}
\text{action} & ::= \text{snd} \_ \text{rcv} | \tau \\
\text{snd} \_ \text{rcv} & ::= \text{message} \ '!' \ \text{address} \\
\text{address} & ::= \text{EID}|\text{mem}\_\text{op}[\text{;};\text{mem}\_\text{op}]^* \\
\text{mem}\_\text{op} & ::= '>$\text{var}$ | '<$\text{var}$ | var'+' | var'-' | \\
& \quad [\text{var}] | '>'\text{var}'+'$ | '>'\text{var}'-'$ | \\
& \quad '<$\text{var}'+'$ | '<$\text{var}-'$
\end{align*}
$$

Some messages usually have parameters—in that case, they are separated by commas($,$). Recall that $?$ means get and $!'$ means put and empty action refers to $\tau$. var represents a variable name. The syntax for local operations on variables is given in the next subsection.

The automaton performing the get/put action on the shared buffer $B$ has atomic access to the latter since a transition is deemed complete only after the action has been fully carried out.

### 3.6.3 Definition of LOS function

For a transition $t = (src, (tr, ac, td), dest) \in \rightarrow_A$, the LOS function $f_t : \{1, ..., N\} \rightarrow MemAct$, returns an ordered sequence of operations that must be performed on variables while the transition $t$ is taken—$N$ is the number of operations to perform. As the name suggests, a LOS function defines operations on local variables only.

For any variable $v \in V_{\eta(A)}$ with value $\xi(v) = (d, R)$, the set $MemAct$ consists of the following operations:

$\text{fetch}(v)$ Fetches the value of $v$ and puts it in a set, i.e. $\{\text{val}(v)\}$, which is $\{d\}$. Syntax: $<v$. 


enlist($v$) Fetches the set of all remembered values, i.e. $\text{buffer}(v)$, which is $R$. Syntax: $[v]$.

update($v$) Requests the update of the variable $v$ based on the sender of a message. Syntax: $>_v$.

remember($v$) Adds the value of $v$ to the set of remembered values, i.e. after remember($v$), $v$ becomes $(d, R \cup \{d\})$. Syntax: $v+$.

forget($v$) Removes the value of $v$ from the set of remembered values, i.e. after forget($v$), $v$ becomes $(d, R \setminus \{d\})$. Syntax: $v-$.

Hence, we have $\text{MemAct} = \{\text{op} | \exists v \in \mathcal{V}_{R(A)} : \text{op} = \text{fetch}(v) \lor \text{op} = \text{enlist}(v) \lor \text{op} = \text{update}(v) \lor \text{op} = \text{remember}(v) \lor \text{op} = \text{forget}(v)\}$.

The operations are executed sequentially while the transition $t$ is taken, i.e. $f_t(1)$ is executed, followed by $f_t(2)$ and so on. Only certain sequence of operations are allowed based on the action $ac$; $f_t$ is a valid function iff:

- If $ac$ is a put action, the $f_t$ function can only contain fetch and/or enlist, i.e. $\exists msg \in \mu : ac = \text{put}(msg, f_t) \Rightarrow \forall i \in \{1, 2, ..., |f_t|\}, \exists v \in \mathcal{V} : f_t(i) = \text{enlist}(v) \lor f_t(i) = \text{fetch}(v)$.

- If $ac$ is a get action, there must be at least one update but no fetch or atmost one fetch, which must also be first, i.e. $\exists msg \in \mu : ac = \text{get}(msg, f_t) \Rightarrow (\exists i \in \{1, 2, ..., |f_t|\}, v \in \mathcal{V} : f_t(i) = \text{update}(v) \land \forall v \in \mathcal{V}, i \in \{1, 2, ..., |f_t|\} : f_t(i) \neq \text{fetch}(v) \lor (\forall i \in \{1, 2, ..., |f_t|\} \exists v \in \mathcal{V} : f_t(i) = \text{fetch}(v) \Rightarrow i = 1))$.

Some of the operations can be combined syntactically, e.g. $>_v +$ stands for $\{(1, \text{update}(v)), (2, \text{remember}(v))\}$.

### 3.6.4 Trigger

For the transition $(\text{src}, (tr, ac, td), dest) \in \rightarrow_A$, the trigger $tr$ is a boolean combination of predicates defined on variables or states. Predicates are side-effect free and can be connected by $\land$ and/or $\lor$. The unary boolean operator $\neg$ is also supported. Specifically, given the current configuration $C$, $v \in \mathcal{V}$ and $s \in \bigcup_{A' \in \mathcal{A}} \Sigma_{A'}$, $tr$ is a monomial over the following propositions:

empty($v$) evaluates to true iff $|\text{enlist}(v)| = 0$.

nempty($v$) evaluates to true iff $|\text{enlist}(v)| > 0$. 

in(s) Evaluates to true iff $s \in C$.

visited(s) Evaluates to true iff $s$ has been visited at least once, i.e. $s \in T$.

Note that the $in(s)$ predicate restricts the enabledness of a transition based on the state $s$ and thus can be used together with $td$ to simulate inter-level transitions. It can also be used to introduce causal dependencies between entities for synchronization.

**Example 7** The definition of $\mu$ used in the examples is: $\mu = \{CCC, CCCR, ContextRefused, RegisterResponse, Prepare, Prepared, ReadOnly, Commit, Committed, Rollback, Aborted\} \times \{\epsilon\} \cup \{Register\} \times \{completion, volatile, durable\}$. Due to space constraints, we will only give the set of labels $\Lambda$ for the Coordinator automaton.

$$\Lambda_{Coordinator} = \{(true, get(\text{CCC}, \epsilon, I), \phi),
\text{(true, put(\text{CCCR}, \epsilon, I), \phi)},
\text{(in(r2), get(\text{Commit, \epsilon, I, \phi)}),}
\text{(in(r2), get(\text{Rollback, \epsilon, I, \{a0\}}),}
\text{(in(m1) \lor in(m3), get(\text{Aborted, \epsilon, f_t)}), \phi)}\}$$

where $f_t = \{(1, update(v)), (2, forget(v)), (3, update(d)), (4, forget(d))\}$.

## 4 Semantics of ASEHA

We define the semantics of ASEHA using a Kripke structure, $\mathcal{K}$, used by many model checkers for verification. Once we translate ASEHA into Promela (the input language of SPIN), we will have show that the Kripke structure, $\mathcal{K}'$, used by SPIN is bisimilar to $\mathcal{K}$ to make sure our translation is correct. However, this will not be further addressed in this paper.

To define $\mathcal{K}$, we first introduce the notion of enabled transitions.

### 4.1 Definition of Enabled Transitions

Given an ASEHA $\mathcal{H} = (\mathcal{F}, \mu, V, \gamma, \eta)$, to determine which transitions are enabled at any time, we need to know the configuration $C$ of $\mathcal{H}$, the message buffer $\mathcal{B}$, the visited list $T$, and the evaluation function $\xi$ on $(V, D)$. Collectively, we refer to $(C, \mathcal{B}, T, \xi)$ as a status in the rest of this paper.

A transition $t = (s, (tr, ac, td), s') \in A$ is enabled in the status $(C, \mathcal{B}, T, \xi)$, denoted by $enabled_{(C,\mathcal{B},T,\xi)}(t)$, iff:

- $s$ is active, i.e. $s \in C$. 
\( tr \) evaluates to true, i.e. \((C, B, T, \xi) \models tr\).

- \( ac \) is unblocked, i.e. for some \( d \in D, v \in V \) and LOS function \( f_t \):
  - \( ac = get(msg, d) \Rightarrow (d, msg, \eta(A)) \in B \)
  - \( (ac = get(msg, f_t) \land f_t(1) = fetch(v)) \Rightarrow (val(v), msg, \eta(A)) \in B \)
  - \( (ac = get(msg, f_t) \land f_t(1) \neq fetch(v)) \Rightarrow (d, msg, \eta(A)) \in B \)

The set of enabled transitions in \((C, B, T, \xi)\) is given by:

\[
ET_{(C, B, T, \xi)} = \left\{ t \mid t \in \bigcup_{A \in F} \rightarrow_A \land enabled_{(C, B, T, \xi)}(t) \right\}
\]

**Example 8** Consider the status \((C, B, T, \xi) = (\{w0, c2, r2, i4, p1\}, \{(C, RegisterResponse, e), P)\}, \{w0, p0, p1, i0, i1, i2, i3, i4, c0, c1, c2, r0, r1, r2, r3\}, \{(v, (P, \{P\}), (d, (C, \phi)), (p, (C, \phi)))\}).

This means that currently: the states \(w0, c2, r2, i4, p1\) are active, the shared buffer contains the tuple \((C, (RegisterResponse, e), P)\), the states \(w0, p0, p1, i0, i1, i2, i3, i4, c0, c1, c2, r0, r1, r2, r3\) have been visited and the variable \(v\) has the value \(P\) and has remembered \(P\).

In this status, the set of enabled transitions is

\[
ET_{(C, B, T, \xi)} = \{(p1, (true, get((RegisterResponse, e), C), \phi), p3), (r2, (true, put((Rollback, e), C), \phi), r4)\}
\]

### 4.2 Definition of Kripke Structure

The semantics of \(H = (F, \mu, V, \gamma, \eta)\) is a Kripke structure \(K = (S, s_0, F, ^{STEP})\), where:

- \( S \subseteq Conf(\gamma) \times D \times V \times D \times \Sigma_F \times 2^V \times (D \times D) \) is the set of all possible statuses of \(H\), which is a subset of the cross-product of all configurations, contents of the message buffer, visited states and variables with their corresponding possible values.

- \( s_0 \in S \) is the initial status, given by:

\[
s_0 = (Conf_{\gamma_{root}}, \phi, Conf_{\gamma_{root}}, \{(v, (d, \phi)) \mid d \in D \land v \in V\})
\]
• $F \subseteq S$ is the set of final (accepting) states derived as follows:

$$(C, B, T, \xi) \in (F \cap S) \iff \forall s \in C \exists A \in F : s \in \Phi_A$$

This means that a state in $K$ is accepting iff all states in the configuration are final states of automata in $H$.

• $\text{STEP} \subseteq S \times S$ is the transition relation of $K$, where $(C, B, T, \xi)^{\text{STEP}} (C', B', T', \xi')$ iff there exists a transition $\{t\} = \{(s, (tr, ac, td), s')\} \in (ET(C, B, T, \xi) \cap \rightarrow_A)$ for some $A \in F$ such that:

$$C' = \begin{cases} 
    \text{head} \cup \{s'\} \cup td & \text{if } td \neq \phi \\
    C & \text{otherwise}
\end{cases}$$

where $\text{head} = C \setminus \left( \bigcup_{A' \in \theta'(A)} \Sigma_{A'} \right)$ and $\{C\} = \{G | G \in Conf(\gamma) \land (\text{head} \cup \{s'\} \subseteq G) \land \forall x \in G \setminus (\text{head} \cup \{s'\}), \exists A' \in F : x = i_{A'}\}$

The new configuration $C'$ is obtained by:

- exiting the state $s$ and all the states below it, i.e. $\left( \bigcup_{A' \in \theta'(A)} \Sigma_{A'} \right)$
- entering $s'$ i.e. $\{s'\}$
- entering $td$ if it is specified or entering the default states of the automata below $s'$, if $td$ is unspecified.

To get the contents of the new shared buffer $B'$, the action $ac$ is carried out using the definitions in Sections 3.6.2 and 3.6.3.

$$B' = \begin{cases} 
    B \cup \{(\eta(A), msg, d)\} & \text{if } ac = \text{put}(msg, d) \\
    B \setminus \{(d, msg, \eta(A))\} & \text{if } ac = \text{get}(msg, d) \\
    B^p & \text{if } ac = \text{put}(msg, f_t) \\
    B^g & \text{if } ac = \text{get}(msg, f_t) \\
    B & \text{otherwise}
\end{cases}$$

for some $msg \in \mu$, $d \in D$ and LOS function $f_t$. $B^p$ and $B^g$ are obtained by sequentially ‘processing’ the operations in the LOS function $f_t$.

Let $B^p_{0..|f_t|}$ be an array of set of tuples, with $B^p_0 = B$ and $B^p_{1..|f_t|}$ be defined by:

$$B^p_i = B^p_{i-1} \cup \begin{cases} 
    \{(\eta(A), msg, val(v))\} & \text{if } f_t(i) = \text{fetch}(v) \\
    \{(\eta(A), msg, x) | x \in buffer(v)\} & \text{if } f_t(i) = \text{enlist}(v) \\
    \phi & \text{otherwise}
\end{cases}$$
5 FEATURES OF ASEHA

for some $v \in \mathcal{V}$. $B_p$ is simply given by $B_p = B^p_{\{f_1\}}$.

$$B^g = B \setminus \begin{cases} 
\{(\text{val}(v), \text{msg}, \eta(A))\} & \text{if } f_1(1) = \text{fetch}(v) \\
\{(\text{addr}, \text{msg}, \eta(A))\} & \text{otherwise}
\end{cases}$$

where $\text{addr}$ is an arbitrary value such that $\text{addr} \in \mathcal{D}$ and $(\text{addr}, \text{msg}, \eta(A)) \in B$.

The new set of visited states $T'$ is obtained by adding the states in the new configuration $C'$ to the old set of visited states $T$.

$$T' = T \cup C'$$

Let $\xi_{0..\lfloor f_1 \rfloor}$ be an array of evaluation functions, with $\xi_0 = \xi$ and $\xi_{1..\lfloor f_1 \rfloor}$ defined by:

$$\xi_i = \xi_{i-1} \setminus \{(v, (d, R))\} \cup \begin{cases} 
\{(v, (\text{addr}, R))\} & \text{if } f_1(i) = \text{update}(v) \\
\{(v, (d, R \cup \{d\}))\} & \text{if } f_1(i) = \text{remember}(v) \\
\{(v, (d, R \setminus \{d\}))\} & \text{if } f_1(i) = \text{forget}(v) \\
\{(v, (d, R))\} & \text{otherwise}
\end{cases}$$

where $v \in \mathcal{V}$, $(d, R) = \xi_{i-1}(v)$ and $\text{addr}$ obtained from the definition of $B^g$ (above). Thus, $\xi'$ is simply given as $\xi' = \xi_{\lfloor f_1 \rfloor}$ after all operations have been performed on the variables.

The states of $\mathcal{K}$ are thus all the possible statuses of $\mathcal{H}$.

**Example 9** Continuing from previous examples, the first few states of the resulting Kripke structure is shown in Figure 7.

5 Features of ASEHA

ASEHA has many desirable features which include:

**Communication** One of the distinguishing aspects about ASEHA compared to other existing formalisms, is that the communication mechanism is based on shared buffer model using interleaving semantics for step execution. This model is captures all the key information without going into unnecessary details.
Simplicity We believe that simplicity is the most important factor for a formalism to be accepted and used by people who do not have an in depth knowledge of formal methods. The diagrams generated by our tool are very simple and intuitive for any programmer or protocol designer to pick up and understand within minutes. Furthermore, the semantics is very simple: one step in ASEHA is just one transition.

Reusability Automata defined in ASEHA are easily reusable; one just has to link a state to an automaton or group of automata for hierarchical composition.

Hierarchy Due to the hierarchical nature of ASEHA, different levels of abstraction are provided by zoom in and zoom out mechanisms. This is based on the tried and tested ideas of David Harel [11].

Permutation Handling As the number of entities increases, the number of possible message exchange sequences can increase exponentially. To be able to represent exchanges concisely, we have variable support.

Verification ASEHA’s simple semantics results into almost straightforward generation of verification code while leaving room for clever optimizations.

Last but not least, ASEHA’s formalism can easily be extended by adding new predicates (for triggers) or data-structures (for LOS function) and can be easily adjusted for use in other domains.

6 Case Study: WS-AT

We have successfully modelled and verified WS-AT. Its purpose is to ensure that a transaction, done across several trusted WS providers, is atomic[8, 14].

We used our own XML-based language called ASEHAX to encode the ASEHA for the WS-AT protocol. In the model, we have assumed unexpected messages (messages arriving out of order or non-protocol messages) are discarded for simplicity. The parser, written in the Java language for ASEHAX, reads the input and automatically generates diagrams for each automaton using *dotty* [15]. The resulting diagrams have been shown earlier (Figures 2, 3, 4, 5), which have been slightly modified for clarity.

We varied the number of participants from 1 to 4. During this process, we also had to adjust the causal dependencies between the initiator and participant and the triggers for entering the final states of *WSAT* automaton. The initiator can only request transaction commitment, by sending the *Commit* message (*i*4 to *i*5), after the desired participants have successfully registered with the coordinator. We had
to introduce more causal dependencies to allow participants, which registered for volatile 2PC, to allow other participants to register for durable 2PC before sending the Prepared message ($p_5$ to $p_6$). For more information, consult [8, 14]. Verification code in Promela was generated for each case and then input to SPIN. Correctness was determined by observing that one of the states $co$, $ab$ and $ex$ was entered (which represent the whether a transaction is committed, aborted, or exited prematurely, respectively) and that all these states were reachable.

7 Conclusions and Future Work

In this paper we have presented ASEHA, the formalism for modelling WS protocols, which integrates many desirable features while still having a simple syntax and semantics. We modelled the essential components of protocols and described how their correctness can be verified using Kleene structures.

This formalism has been used to model the WS-AT protocol and aid in its formal specification thereby partially solving the interoperability problem. In the next paper of this sequel, we will demonstrate the translation of ASEHA to Promela and the verification process in details. Some work in progress include protocol compatibility checking (i.e. making sure that implementations actually correctly implement the protocols and that they can interact correctly) and graphical support for end-users.

Acknowledgments

National ICT Australia is funded through the Australian Government's initiative, in part through the Australian Research Council. We also thank David Langworthy of Microsoft, Tim Bourke and Piyush Maheshwari of School of Computer Science and Engineering, University of New South Wales, for their valuable comments and feedback.

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Figure 7: First few states of the resulting Kripke structure