# Session Type Isomorphisms

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There has been a considerable amount of work on retrieving functions in function libraries using their type as search key. The availability of rich component specifications, in the form of behavioral types, enables similar queries where one can search a component library using the behavioral type of a component as the search key. Just like for function libraries, however, component libraries will contain components whose type differs from the searched one in the order of messages or in the position of the branching points. Thus, it makes sense to also look for those components whose type is different from, but isomorphic to, the searched one.

In this article we give semantic and axiomatic characterizations of isomorphic session types. The theory of session type isomorphisms turns out to be subtle. In part this is due to the fact that it relies on a non-standard notion of equivalence between processes. In addition, we do not know whether the axiomatization is complete. It is known that the isomorphisms for arrow, product and sum types are not finitely axiomatisable, but it is not clear yet whether this negative results holds also for the family of types we consider in this work.

### **1** Introduction

We have all experienced, possibly during a travel abroad, using an ATM that behaves differently from the ones we are familiar with. Although the information requested for accomplishing a transaction is essentially always the same – the PIN, the amount of money we want to withdraw, whether or not we want a receipt – we may be prompted to enter such information in an unexpected order, or we may be asked to dismiss sudden popup windows containing informative messages – "charges may apply" – or commercials. Subconsciously, we *adapt* our behavior so that it matches the one of the ATM we are operating, and we can usually complete the transaction provided that the expected and actual behaviors are *sufficiently similar*. An analogous problem arises during software development or execution, when we need a component that exhibits some desired behavior while the components we have at hand exhibit similar, but not exactly equal, behaviors which could nonetheless be adapted to the one we want. In this article, we explore one particular way of realizing such adaptation in the context of binary sessions, where the behavior of components is specified as session types.

There are two key notions to be made precise in the previous paragraph: first of all, we must clarify what it means for two behaviors to be "similar" to the point that one can be adapted into the other; second, as for the "subconscious" nature of adaptation, we translate this into the ability to synthesize the adapter automatically – *i.e.* without human intervention – just by looking at the differences between the required and actual behaviors of the component. Clearly we have to find a trade-off: the coarser the similarity notion is the better, for this means widening the range of components we can use; at the same time, it is reasonable to expect that the more two components differ, the harder it gets to automatically synthesize a sensible adapter between them. The methodology we propose in this work is based on the theory of *type isomorphisms* [10]. Intuitively, two types *T* and *S* are isomorphic if there exist two adapters *A* :  $T \rightarrow S$  and *B* :  $S \rightarrow T$  such that *A* transforms a component of type (or, that behaves like) *T* into one of type *S*, and *B* does just the opposite. It is required that these transformations must not *lose any information*. This

can be expressed saying that if we compose *A* and *B* in any order they annihilate each other, that is we obtain adapters  $A \parallel B : T \to T$  and  $B \parallel A : S \to S$  that are equivalent to the "identity" trasformations on *T* and *S* respectively.

In the following we formalize these concepts: we define syntax and semantics of processes as well as a notion of process equivalence (Section 2). Next, we introduce a type system for processes, the notion of session type isomorphism, and show off samples of the transformations we can capture in this framework (Section 3). We conclude with a quick survery of related works and open problems (Section 4).

#### 2 Processes

We let  $m, n, \ldots$  range over integer numbers; we let c range over the set  $\{1, r\}$  of *channels* and  $\ell$  range over the set  $\{inl, inr\}$  of *selectors*. We define an involution  $\overline{\cdot}$  over channels such that  $\overline{l} = r$ . We assume a set of *basic values* v, ... and *basic types* t, s, ... that include the unitary value () of type unit, the booleans true and false of type bool, and the integer numbers of type int. We write  $v \in t$  meaning that v has type t. We use a countable set of *variables* x, y, ...; *expressions* e, ... are either variables or values or the equality  $e_1 = e_2$  between two expressions. Additional expression forms can be added without substantial issues. *Processes* are defined by the grammar

$$P ::= \mathbf{0} \mid \mathsf{c}?(x:t).P \mid \mathsf{c}!\langle \mathsf{e} \rangle.P \mid \mathsf{c} \triangleleft \ell.P \mid \mathsf{c} \triangleright \{P,Q\} \mid \mathsf{if} \mathsf{e} \mathsf{then} P \mathsf{else} Q \mid P \llbracket Q$$

which includes the terminated process **0**, input c?(x:t).P and output  $c!\langle e \rangle.P$  processes, as well as labeled-driven selection  $c \triangleleft \ell.P$  and branching  $c \triangleright \{P,Q\}$ , the conditional process if e then P else Q, and parallel composition  $P \parallel Q$ . The peculiarity of the calculus is that communication occurs only between adjacent processes. Such communication model is exemplified by the diagram below which depicts the composition  $P \parallel Q$ . Each process sends and receives messages through the channels 1 and r. Messages sent by P on r are received by Q from 1,

and messages sent by Q on 1 are received by P from  $\mathbf{r}$ . Therefore, unlike more conventional parallel composition operators,  $\| \mathbf{f} \|$  is associative but not symmetric in general. Intuitively,  $P \| \mathbf{f} Q$  models a binary session where P and Q are the processes accessing the two end-



points of the session. By compositionality, we can also represent more complex scenarios like  $P \int A \int Q$  where the interaction of the same two processes *P* and *Q* is mediated by an adapter *A* that filters and/or transforms the messages exchanged between *P* and *Q*. In turn, *A* may be the parallel composition of several simpler adapters.

The operational semantics of processes is formalized as a reduction relation closed by reduction contexts and a structural congruence relation. *Reduction contexts*  $\mathscr{C}$  are defined by the grammar

$$\mathscr{C} ::= [] | \mathscr{C} \llbracket P | P \rrbracket \mathscr{C}$$

and, as usual, we write  $\mathscr{C}[P]$  for the process obtained by replacing the hole in  $\mathscr{C}$  with P.

Structural congruence is the least congruence defined by the rules

$$\mathbf{0} \parallel \mathbf{0} \equiv \mathbf{0} \qquad P \parallel (Q \parallel R) \equiv (P \parallel Q) \parallel R$$

while <u>reduction</u> is the least relation  $\longrightarrow$  defined by the rules in Table 1. The rules are familiar and therefore unremarkable. We assume a deterministic <u>evaluation</u> relation  $e \downarrow v$  expressing the fact that v

[R-COMM 1]	[R-COMM	[R-COMM 2]			
$e \downarrow v \qquad v \in t$		$e \downarrow v \qquad v \in t$			
$\mathbf{r!} \langle \mathbf{e} \rangle . P  \mathrm{llr}(x:t) . Q \longrightarrow P  \mathrm{llr}Q$	$\frac{\mathbf{r}}{\mathbf{v}/x} \qquad \mathbf{r} \cdot (x:t)$	$\mathbf{r}?(x:t).P \sqsubseteq \mathbf{l}! \langle \mathbf{e} \rangle.Q \longrightarrow P\{\mathbf{v}/x\} \sqsubseteq Q$			
$ [\texttt{R-CHOICE 1}] \\ \texttt{r} \triangleleft \ell.P  ] [\texttt{l} \triangleright \{ Q_{\texttt{inl}}, Q_{\texttt{inr}} \} \longrightarrow $	$P \rfloor [Q_{\ell}] \qquad r \triangleright \{P_{in}\}$	CE 2] $P_1, P_{inr} \} \   1 \triangleleft \ell. Q \longrightarrow P_{\ell}   $	Q		
[R-COND]	[R-CONTEXT]	[R-STRUCT]			
$e \downarrow v$ $v \in bool$	$P \longrightarrow Q$	$P \equiv P' \qquad P' \longrightarrow Q'$	$Q'\equiv Q$		
if e then $P_{\text{true}}$ else $P_{\text{false}} \longrightarrow P_{v}$	$\mathscr{C}[P] \longrightarrow \mathscr{C}[Q]$	$P \longrightarrow Q$			

Table 1: Reduction relation.

is the value of e. We write  $\longrightarrow^*$  for the reflexive, transitive closure of  $\longrightarrow$  and  $P \nleftrightarrow$  if there is no Q such that  $P \longrightarrow Q$ . With these notions we can characterize the set of correct processes, namely those that complete every interaction and eventually reduce to **0**:

**Definition 1** (correct process). We say that a process *P* is *correct* if  $P \longrightarrow^* Q \nleftrightarrow$  implies  $Q \equiv \mathbf{0}$ .

A key ingredient of our development is a notion of process equivalence that relates two processes P and Q whenever they can be completed by the same contexts  $\mathscr{C}$  to form a correct process. Formally:

**Definition 2** (equivalence). We say that two processes *P* and *Q* are *equivalent*, notation  $P \approx Q$ , whenever for every  $\mathscr{C}$  we have that  $\mathscr{C}[P]$  is correct if and only if  $\mathscr{C}[Q]$  is correct.

Note that the relation  $\approx$  differs from more conventional equivalences between processes. In particular,  $\approx$  is insensitive to the exact time when visible actions are made available on the two interfaces of a process. For example, we have

$$1?(x:int).r!\langle true \rangle.1?(y:unit) \approx 1?(x:int).1?(y:unit).r!\langle true \rangle$$
(1)

despite the fact that the two processes perform visible actions in different orders. Note that the processes in (1) are not (weakly) bisimilar.

#### **3** Type System and Isomorphisms

Session types T, S, ... are defined by the grammar

$$T ::= end \mid ?t.T \mid !t.T \mid T+S \mid T \oplus S$$

and are fairly standard, except for branching T + S and selection  $T \oplus S$  which are binary instead of *n*-ary operators, consistently with the process language. As usual, we denote by  $\overline{T}$  the *dual* of *T*, namely the session type obtained by swapping inputs with outputs and selections with branches in *T*.

We let  $\Gamma$  range over *environments* which are finite maps from variables to types of the form

$$x_1:t_1,\ldots,x_n:t_n.$$

The typing rules are given in Table 2. Judgments have the form:

•  $\Gamma \vdash e : t$  stating that e is well typed and has type t in the environment  $\Gamma$  and

Table 2:	Typing	rules	for	expressions	and	processes.
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			•	-	
[[]] ]	[T-VALUE]	[T-eq]		[T-INPUT]	
[I-VAR]	$v \in t$	$\Gamma \vdash e_1 : t$	$\Gamma \vdash e_2 : t$	$\Gamma, x: t \vdash P \blacktriangleright \{c: T, \overline{c}: S\}$	
$\Gamma, x: t \vdash x: t$	$\overline{\Gamma \vdash v : t}$	$\Gamma \vdash e_1 = e_2: \texttt{bool}$		$\Gamma \vdash c?(x:t).P \blacktriangleright \{c:?t.T,\overline{c}:S\}$	
[T-OU	ΓPUT]		[T-BRANCH	H]	
$\Gamma \vdash e: t \qquad \Gamma \vdash P \blacktriangleright \{c: T, \overline{c}: S\}$			$\Gamma \vdash P_i \blacktriangleright \{c: T_i, \overline{c}: S\}^{(i=1,2)}$		
$\Gamma \vdash c ! \langle e \rangle . P \blacktriangleright \{c : !t.T, \overline{c} : S\} \qquad \overline{\Gamma \vdash c \triangleright \{P_1, P_2\}} \blacktriangleright \{c : T_1 + T_2, \overline{c} : S\}$					
[T-SELECT LEFT]		[T-SELECT	RIGHT]		
$\Gamma \vdash P \blacktriangleright \{c: T_1, \overline{c}: S\}$		$\Gamma \vdash I$	$\Gamma \vdash P \blacktriangleright \{c: T_2, \overline{c}: S\} \qquad [T-IDLE]$		
$\overline{\Gamma \vdash c \triangleleft inl.P \blacktriangleright \{c: T_1 \oplus T_2, \overline{c}: S\}}  \overline{\Gamma \vdash c \triangleleft inr.P \blacktriangleright \{c: T_1 \oplus T_2, \overline{c}: S\}}  \Gamma \vdash 0 \blacktriangleright \{1: end, r: end\}$					
[T-CONDITIONAL]		<i>.</i>	[T-PARALLE	EL]	
$\Gamma \vdash e: bool$ $\Gamma$	$\Gamma \vdash P_i \blacktriangleright \{ \texttt{l} : T, \texttt{p}\}$	$: S \} \stackrel{(i=1,2)}{\ldots}$	$\Gamma \vdash P \blacktriangleright \{1$	$: T, \mathbf{r} : T'$ $\Gamma \vdash Q \triangleright \{\mathbf{l} : \overline{T}', \mathbf{r} : S\}$	
$\Gamma \vdash \texttt{if e then } P_1 \texttt{ else } P_2 \blacktriangleright \{\texttt{l}: T, \texttt{r}: S\}$			$\Gamma \vdash P  \rfloor \!\! \int Q \blacktriangleright \{ \texttt{l} : T, \texttt{r} : S \}$		

•  $\Gamma \vdash P \triangleright \{c : T, \overline{c} : S\}$  stating that *P* is well typed in the environment  $\Gamma$  and uses channel c according to *T* and  $\overline{c}$  according to *S*.

**Theorem 1.** If  $\vdash P \triangleright \{1 : end, r : end\}$ , then *P* is correct.

*Proof.* Looking at the typing rules it is clear that *P* can only be **0**, or a conditional or a parallel composition. The first two case are immediate. In the third case let *P* be  $P_1 \parallel \cdots \parallel P_i \parallel \cdots \parallel P_n$ , where  $P_1, \ldots, P_i, \ldots, P_n$  are single-threaded. Then rule [T-PARALLEL] requires

$$\vdash P_1 \blacktriangleright \{1: \text{end}, \mathbf{r}: T_1\}, \vdash P_i \blacktriangleright \{1: \overline{T_{i-1}}, \mathbf{r}: T_i\} \text{ for } 2 \leq i \leq n-1 \text{ and } \vdash P_n \blacktriangleright \{1: \overline{T_{n-1}}, \mathbf{r}: \text{end}\}$$

for some types  $T_1, \ldots, T_{n-1}$ . The proof is by induction on  $T_1, \ldots, T_{n-1}$ . The first step coincides with the first case. For the induction step we can assume that  $P_1, \ldots, P_i, \ldots, P_n$  are not conditionals, since otherwise at least one of them could be reduced by rule [R-COND]. Notice that **r** is the only channel in  $P_1$  and **1** is the only channel in  $P_n$ . Then there must be at least one index j ( $1 \le j \le n-1$ ) such that  $P_j$  starts with a communication/selection/branching on channel **r** and  $P_{j+1}$  starts with a communication/selection/branching on channel **1**. We only consider the case  $T_j = T_{inl} \oplus T_{inr}$ , the proofs for the other cases being similar. Rules [T-SELECT LEFT], [T-SELECT RIGHT] and [T-BRANCH] require  $P_j \equiv \mathbf{r} \triangleleft \ell. Q$ and  $P_{j+1} \equiv \mathbf{l} \triangleright \{Q_{inl}, Q_{inr}\}$ . Therefore  $P \longrightarrow P_1 \parallel \ldots \parallel Q \parallel Q_\ell \parallel \ldots \parallel P_n$  by rules [R-CHOICE 1] and [R-CONTEXT]. This concludes the proof, since  $\vdash Q \blacktriangleright \{\mathbf{1} : \overline{T_{j-1}}, \mathbf{r} : T_{\ell}\}, \vdash Q_\ell \blacktriangleright \{\mathbf{1} : \overline{T_{\ell}}, \mathbf{r} : T_{j+1}\}$ .

To have an isomorphism between two session types T and S, we need a process A that behaves according to  $\overline{T}$  on its left interface and according to S on its right interface. In this way, the process "transforms" T into S. Symmetrically, there must be a process B that performs the inverse transformation. Not all of these transformations are isomorphisms, because we also require that these transformations must not entail any *loss of information*. Given a session type T, the simplest process with this property is the *identity* process id<sub>T</sub> defined below:

$$id_{end} = 0 \qquad id_{!t.T} = 1?(x:t).r! \langle x \rangle.id_T \qquad id_{T \oplus S} = 1 \triangleright \{r \triangleleft inl.id_T, r \triangleleft inr.id_S\} \\ id_{?t.T} = r?(x:t).l! \langle x \rangle.id_T \qquad id_{T+S} = r \triangleright \{l \triangleleft inl.id_T, l \triangleleft inr.id_S\}$$

1.0

Notice that  $\vdash id_T \triangleright \{1: \overline{T}, \mathbf{r}: T\}$ . We can now formalize the notion of session type isomorphism:

**Definition 3** (isomorphism). We say that the session types *T* and *S* are *isomorphic*, notation  $T \cong S$ , if there exist two processes *A* and *B* such that  $\vdash A \triangleright \{1 : \overline{T}, r : S\}$  and  $\vdash B \triangleright \{1 : \overline{S}, r : T\}$  and  $A \rfloor B \approx id_T$  and  $B \rfloor A \approx id_S$ .

**Example 1.** Let  $T \stackrel{\text{def}}{=} ! \text{int.!bool.end}$  and  $S \stackrel{\text{def}}{=} ! \text{bool.!int.end}$  and observe that T and S differ in the order in which messages are sent. Then we have  $T \cong S$ . Indeed, if we take

$$A \stackrel{\text{def}}{=} 1?(x: \text{int}).1?(y: \text{bool}).r! \langle y \rangle.r! \langle x \rangle.0 \quad \text{and} \quad B \stackrel{\text{def}}{=} 1?(x: \text{bool}).1?(y: \text{int}).r! \langle y \rangle.r! \langle x \rangle.0$$

we derive  $\vdash A \triangleright \{1: \overline{T}, r: S\}$  and  $\vdash B \triangleright \{1: \overline{S}, r: T\}$  and moreover  $A \parallel B \approx id_T$  and  $B \parallel A \approx id_S$ .

**Example 2.** Showing that two session types are *not* isomorphic is more challenging since we must prove that there is no pair of processes A and B that turns one into the other without losing information. We do so reasoning by contradiction. Suppose for example that !int.end and end are isomorphic. Then, there must exist  $\vdash A \triangleright \{1:?int.end,r:end\}$  and  $\vdash B \triangleright \{1:end,r:!int.end\}$ . The adapter B is suspicious, since it must send a message of type int on channel r without ever receiving such a message from channel 1. Then, it must be the case that B "makes up" such a message, say it is n (observe that our calculus is deterministic, so B will always output the same integer n). We can now unmask B showing a context that distinguishes  $id_{!int.end}$  from  $A \parallel B$ . Consider

$$\mathscr{C} \stackrel{\text{def}}{=} \mathbf{r}! \langle n+1 \rangle \mathbf{.0} \parallel [] \parallel [1?(x:int).if x = n+1 \text{ then } \mathbf{0} \text{ else } \mathbf{r}! \langle \text{false} \rangle \mathbf{.0}$$

and observe that  $\mathscr{C}[\mathsf{id}_{!\mathsf{int.end}}]$  is correct whereas  $\mathscr{C}[A \mid ]B]$  is not because

$$\mathscr{C}[A \parallel B] \longrightarrow^* \mathbf{0} \parallel \texttt{if } n = n+1 \texttt{ then } \mathbf{0} \texttt{ else } \texttt{r!} \langle \texttt{false} \rangle. \mathbf{0} \longrightarrow \mathbf{0} \parallel \texttt{r!} \langle \texttt{false} \rangle. \mathbf{0} \not \longrightarrow$$

This means that  $A \parallel B \not\approx id_{!int.end}$ , contradicting the hypothesis that A and B were the witnesses of the isomorphism !int.end  $\cong$  end.

**Example 3.** Another interesting pair of non-isomorphic types is given by  $T \stackrel{\text{def}}{=} :$  int.!bool.end and  $S \stackrel{\text{def}}{=} :$  bool.?int.end. A lossless transformation from S to T can be realized by the process

$$B \stackrel{\text{def}}{=} 1?(x:\texttt{bool}).r?(y:\texttt{int}).r!\langle x \rangle.l!\langle y \rangle.0,$$

which reads one message from each interface and forwards it to the opposite one. The inverse transformation from T to S is unachieavable without loss of information. Such process necessarily sends at least one message (of type int or of type bool) on one interface *before* it receives the message of the same type from the opposite interface. Therefore, just like in Example 2, such process must guess the message to send, and in most cases such message does not coincide with the one the process was supposed to forward.

Table 3 gathers the session type isomorphisms that we have identified. There is a perfect duality between the odd-indexed axioms (about outputs/selections, on the left) and the even-indexed axioms (about inputs/branchings, on the right), so we briefly discuss the odd-indexed axioms only. Axiom [A1] is a generalization of the isomorphism discussed in Example 1 and is proved by a similar adapter. Axiom [A3] distributes the *same* output on a selection. Basically, this means that the moment of selection is irrelevant with respect to other adjacent output operations. Axiom [A5] shows that sending the unitary value provides no information and therefore is a superfluous operation. Axiom [A7] shows that sending a boolean value is equivalent to making a selection, provided that the continuation does not depend on the

		<b>2</b> I	*	
[A1]	$!t.!s.T \cong !s.!t.T$	[A2]	$?t.?s.T \cong ?s.?t.T$	
[A3]	$!t.(T\oplus S)\cong !t.T\oplus !t.S$	[A4]	$?t.(T+S) \cong ?t.T+?t.S$	
[A5]	!unit. $T \cong T$	[A6]	$?unit.T \cong T$	
[A7]	$!bool.T \cong T \oplus T$	[A8]	?bool. $T\cong T+T$	
[A9]	$T \oplus S \cong S \oplus T$	[A10]	$T + S \cong S + T$	
[A11]	$(T_1\oplus T_2)\oplus T_3\cong T_1\oplus (T_2\oplus T_3)$	[A12]	$(T_1 + T_2) + T_3 \cong T_1 + (T_2 + T_3)$	

Table 3: Session type isomorphisms.

TT 1 1 4	A 1 /	C		•	1.
Table 4	A danters	tor	type	isomor	nhiem
	ruapicis	101	type	13011101	pmsm

$A_1 = 1?(x:t).1?(y:s).r!\langle y \rangle.r!\langle x \rangle.id_T$	$B_1 = 1?(x:s).1?(y:t).r!\langle y \rangle.r!\langle x \rangle.id_T$			
$A_2 = \mathbf{r}?(x:t).\mathbf{r}?(y:s).\mathbf{l}!\langle y \rangle.\mathbf{l}!\langle x \rangle.\mathrm{id}_T$	$B_2 = \mathbf{r}?(x:s).\mathbf{r}?(y:t).\mathbf{l}!\langle y \rangle.\mathbf{l}!\langle x \rangle.id_T$			
$A_3 = 1?(x:t).1 \triangleright \{ r \triangleleft inl.r! \langle x \rangle.id_T, r \triangleleft inr.r! \langle x \rangle.id_T \}$	$x \rangle.id_S$			
$B_3 = 1 \triangleright \{1?(x:t).r! \langle x \rangle.r \triangleleft \texttt{inl.id}_T, 1?(x:t).r!$	$\langle x \rangle$ .r < inr.id <sub>S</sub> }			
$A_4 = \mathbf{r} \triangleright \{\mathbf{r}?(x:t).1! \langle x \rangle.1 \triangleleft \mathtt{inl.id}_T, \mathbf{r}?(x:t).1!$	$\langle x \rangle$ .l $\triangleleft$ inr.id <sub>S</sub> }			
$B_4 = \mathbf{r}?(x:t).\mathbf{r} \triangleright \{ 1 \triangleleft \mathtt{inl.l}! \langle x \rangle. \mathtt{id}_T, 1 \triangleleft \mathtt{inr.l}! \langle x \rangle. \mathtt{id}_T \}$	$x \rangle.id_S$			
$A_5 = 1?(x:unit).id_T$	$B_5 = \mathbf{r}! \langle () \rangle.id_T$			
$A_6 = 1! \langle () \rangle. \mathrm{id}_T$	$B_6 = \mathbf{r}?(x: \mathtt{unit}).id_T$			
$A_7 = 1?(x: bool).if x then r \triangleleft inl.id_T else r$	$\forall \texttt{inr.id}_T \ B_7 = \texttt{l} \triangleright \{\texttt{r}! \langle \texttt{true} \rangle . \texttt{id}_T, \texttt{r}! \langle \texttt{false} \rangle . \texttt{id}_T \}$			
$A_8 = \mathbf{r} \triangleright \{ \mathbf{l} \mid \langle \mathtt{true} \rangle . \mathtt{id}_T, \mathbf{l} \mid \langle \mathtt{false} \rangle . \mathtt{id}_T \} B_8 = \mathbf{r}$	$(x: bool)$ .if x then $1 \triangleleft inl.id_T$ else $1 \triangleleft inr.id_T$			
$A_9 = l \triangleright \{ \mathbf{r} \triangleleft \mathbf{inr.id}_T, \mathbf{r} \triangleleft \mathbf{inl.id}_S \}$	$B_9 = l \triangleright \{ r \triangleleft inr.id_S, r \triangleleft inl.id_T \}$			
$A_{10} = \mathbf{r} \triangleright \{ \mathbf{l} \triangleleft \mathbf{inr.id}_S, \mathbf{l} \triangleleft \mathbf{inl.id}_T \}$	$B_{10} = \mathbf{r} \triangleright \{ \mathbf{l} \triangleleft \mathbf{inr.id}_T, \mathbf{l} \triangleleft \mathbf{inl.id}_S \}$			
$A_{11} = 1 \triangleright \{1 \triangleright \{r \triangleleft \texttt{inl.id}_{T_1}, r \triangleleft \texttt{inr.r} \triangleleft \texttt{inl.id}_{T_2}\}, r \triangleleft \texttt{inr.r} \triangleleft \texttt{inr.id}_{T_3}\}$				
$B_{11} = l \triangleright \{ r \triangleleft inl.r \triangleleft inl.id_{T_1}, l \triangleright \{ r \triangleleft inl.r \triangleleft inr.id_{T_2}, r \triangleleft inr.id_{T_3} \} \}$				
$A_{12} = \mathbf{r} \triangleright \{ 1 \triangleleft \mathtt{inl.l} \triangleleft \mathtt{inl.id}_{T_1}, \mathbf{r} \triangleright \{ 1 \triangleleft \mathtt{inl.l} \triangleleft \mathtt{inr.id}_{T_2}, 1 \triangleleft \mathtt{inr.id}_{T_3} \} \}$				
$B_{12} = \mathbf{r} \triangleright \{ \mathbf{r} \triangleright \{ \mathbf{l} \triangleleft \mathtt{inl.id}_{T_1}, \mathbf{l} \triangleleft \mathtt{inr.l} \triangleleft \mathtt{inl.id}_{T_2} \}, \mathbf{l} \triangleleft \mathtt{inr.l} \triangleleft \mathtt{inr.id}_{T_3} \}$				

particular boolean value that is sent. In general, any data type with finitely many values can be encoded as possibly nested choices. Axiom [A9], corresponding to the commutativity of  $\oplus$  wrt  $\cong$ , shows that the actual label used for making a selection is irrelevant, only the continuation matters. Axiom [A11], corresponding to the associativity for  $\oplus$  wrt  $\cong$ , generalizes the irrelevance of labels seen in [A9] to nested selections. Since  $\cong$  is a congruence, the axioms in Table 3 can also be closed by transitivity and arbitrary session type contexts.

Table 4 gives all the adapters of the axioms in Table 3. Then the soundness of the axioms in Table 3 amounts to prove:

$$\vdash A_i \triangleright \{1: \overline{T_i}, \mathbf{r}: S_i\} \qquad \vdash B_i \triangleright \{1: \overline{S_i}, \mathbf{r}: T_i\}$$
(2)

$$A_i \rfloor \begin{bmatrix} B_i \approx \mathsf{id}_{T_i} & B_i \rfloor \begin{bmatrix} A_i \approx \mathsf{id}_{S_i} \end{bmatrix}$$
(3)

where  $T_i$  is the l.h.s. and  $S_i$  is the r.h.s. of the axiom [A*i*] for  $1 \le i \le 12$ .

Point 2 can be easily shown by cases on the definitions of  $A_i$  and  $B_i$  taking into account that

$$\vdash \mathsf{id}_T \triangleright \{ \mathbf{l} : \overline{T}, \mathbf{r} : T \}$$



for all types T.

For Point 3 we define a *symbolic reduction relation* which preserves equivalence of closed and typed processes (Theorem 2). This is enough since we will show that all the parallel compositions of the adapters symbolically reduce to the corresponding identities (Theorem 3). The rules of this relation are given in Table 5, where  $\longleftrightarrow$  stands for reduction in both directions and *symbolic reduction contexts*  $\mathscr{E}$  are defined by:

```
 \mathcal{E} ::= [] | c?(x:t).\mathcal{E} | c! \langle e \rangle.\mathcal{E} | c \triangleleft \ell.\mathcal{E} | c \triangleright \{\mathcal{E}, Q\} | c \triangleright \{P, \mathcal{E}\} 
| if e then P else \mathcal{E} | if e then \mathcal{E} else Q
```

We call this a symbolic reduction relation because it also reduces processes with free variables. We notice that this reduction applied to two parallel processes:

- 1. moves up the communications/selections/branchings on the left channel of the left process and the communications/selections/branchings on the right channel of the right process and the conditionals,
- 2. executes the communications/selections/branchings between the right channel of the left process and the left channel of the right process when possible,
- 3. eliminates superfluous identities,

4. swaps communications/selections/branchings on different channels when this is not forbidden by bound variables.

The more interesting rule is [SR-COND], that transforms a conditional in an output.

**Theorem 2.** If *P* is a closed and typed process and  $P \rightsquigarrow^* Q$ , then  $P \approx Q$ .

*Proof.* The proof is by induction on the reduction of Table 5 and by cases on the last applied rule. Notice that the proof for the swap rules is immediate, since these rules can be always reversed. We consider some interesting cases, in which we assume  $R_1 \parallel \mathcal{E} \parallel R_2 \longrightarrow R'_1 \parallel [] \parallel R'_2$  (by extending reduction to contexts in the obvious way) and that  $\{\vec{v}/\vec{y}\}$  are the substitutions made on the hole in this reduction.

[SR-UP 1] If  $R_1 \parallel \mathscr{E}[1?(x:t).P \parallel Q] \parallel R_2$  is correct, then each reduction from  $R_1 \parallel \mathscr{E}[1?(x:t).P \parallel Q] \parallel R_2$  to **0** must be of the shape

$$R_{1} \iint \mathscr{E}[1?(x:t).P \amalg Q] \amalg R_{2} \longrightarrow^{*} R'_{1} \amalg (1?(x:t).P \amalg Q) \{\vec{\mathsf{v}}/\vec{y}\} \amalg R'_{2} \longrightarrow^{*} r! \langle \mathsf{e} \rangle.R \amalg 1?(x:t).P\{\vec{\mathsf{v}}/\vec{y}\} \amalg Q' \longrightarrow^{*} R \amalg P\{\vec{\mathsf{v}}/\vec{y}\} \{\mathbf{v}/x\} \amalg Q' \longrightarrow^{*} \mathbf{0}$$

where  $R'_1 \longrightarrow^* \mathbf{r} \,! \, \langle \mathsf{e} \rangle . R$  with  $\mathsf{e} \downarrow \mathsf{v}, \mathsf{v} \in t$ , and  $Q\{\vec{\mathsf{v}}/\vec{y}\} \, \| R'_2 \longrightarrow^* Q'$ . We get

$$\begin{aligned} R_1 & \int \mathscr{E}[1?(x:t).(P \parallel Q)] & \int R_2 \longrightarrow^* R'_1 \parallel 1?(x:t).(P \parallel Q) \{\vec{v}/\vec{y}\} \parallel R'_2 \longrightarrow^* \\ \mathbf{r}! \langle \mathbf{e} \rangle.R & \int 1?(x:t).(P \parallel Q) \{\vec{v}/\vec{y}\} \parallel R'_2 \longrightarrow R \parallel P\{\vec{v}/\vec{y}\} \{\mathbf{v}/x\} \parallel Q\{\vec{v}/\vec{y}\} \parallel R'_2 \longrightarrow^* \\ R & \prod P\{\vec{v}/\vec{y}\} \{\mathbf{v}/x\} \parallel Q' \longrightarrow^* \mathbf{0} \end{aligned}$$

Vice versa if  $R_1 \parallel \mathscr{E}[1?(x:t).(P \parallel Q)] \parallel R_2$  is correct, then each reduction from  $R_1 \parallel \mathscr{E}[1?(x:t).(P \parallel Q)] \parallel R_2$  to **0** must be of the shape shown above, and the proof concludes similarly.

[SR-UP 7] If  $R_1 \parallel \mathscr{E}[1 \triangleleft inl.P \parallel Q] \parallel R_2$  is correct, then each reduction from  $R_1 \parallel \mathscr{E}[1 \triangleleft inl.P \parallel Q] \parallel R_2$  to **0** must be of the shape

$$\begin{aligned} &R_1 \int \mathscr{E}[1 \triangleleft \texttt{inl}.P \int Q] \int R_2 \longrightarrow R'_1 \int (1 \triangleleft \texttt{inl}.P \int Q) \{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \int R'_2 \longrightarrow R'_1 \\ &\mathsf{r} \triangleright \{P_{\texttt{inl}}, P_{\texttt{inr}}\} \int 1 \triangleleft \texttt{inl}.P\{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \int Q' \longrightarrow P_{\texttt{inl}} \int P\{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \int Q' \longrightarrow 0 \end{aligned}$$

where  $R'_1 \longrightarrow^* r \triangleright \{P_{inl}, P_{inr}\}$  and  $Q\{\vec{v}/\vec{y}\} \parallel R'_2 \longrightarrow^* Q'$ . We get

$$\begin{array}{l} R_1 \parallel \mathscr{C}[1 \triangleleft \mathtt{inl.}(P \parallel Q)] \parallel R_2 \longrightarrow^* R'_1 \parallel 1 \triangleleft \mathtt{inl.}(P \parallel Q) \{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \parallel R'_2 \longrightarrow^* \\ \texttt{r} \triangleright \{P_{\mathtt{inl}}, P_{\mathtt{inr}}\} \parallel 1 \triangleleft \mathtt{inl.}(P \parallel Q) \{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \parallel R'_2 \longrightarrow P_{\mathtt{inl}} \parallel P\{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \parallel Q\{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \parallel R'_2 \longrightarrow^* \\ P_{\mathtt{inl}} \parallel P\{\vec{\mathsf{v}}/\vec{\mathsf{y}}\} \parallel Q' \longrightarrow^* \mathbf{0} \end{array}$$

Vice versa if  $R_1 \parallel \mathscr{E}[1 \triangleleft in1.(P \parallel Q)] \parallel R_2$  is correct, then each reduction from  $R_1 \parallel \mathscr{E}[1 \triangleleft in1.(P \parallel Q)] \parallel R_2$  to **0** must be of the shape shown above, and the proof concludes similarly.

[SR-COND] If  $R_1 \parallel \mathscr{E}[\text{if } x \text{ then } r! \langle \text{true} \rangle . P \text{ else } r! \langle \text{false} \rangle . P] \parallel R_2$  is correct, then each reduction from  $R_1 \parallel \mathscr{E}[\text{if } x \text{ then } r! \langle \text{true} \rangle . P \text{ else } r! \langle \text{false} \rangle . P] \parallel R_2$  to **0** must be of the shape

 $\begin{array}{l} R_1 \int \mathscr{C}[\texttt{if } x \texttt{ then } \texttt{r} ! \langle \texttt{true} \rangle . P \texttt{ else } \texttt{r} ! \langle \texttt{false} \rangle . P] \int R_2 \longrightarrow^* \\ R'_1 \int \texttt{if } \texttt{v} \texttt{ then } \texttt{r} ! \langle \texttt{true} \rangle . P\{\vec{\texttt{v}}/\vec{y}\}\{\texttt{v}/x\} \texttt{ else } \texttt{r} ! \langle \texttt{false} \rangle . P\{\vec{\texttt{v}}/\vec{y}\}\{\texttt{v}/x\} \int R'_2 \longrightarrow^* \\ R'_1 \int \texttt{r} ! \langle \texttt{v} \rangle . P\{\vec{\texttt{v}}/\vec{y}\}\{\texttt{v}/x\} \int R'_2 \longrightarrow^* R'_1 \int \texttt{r} ! \langle \texttt{v} \rangle . P\{\vec{\texttt{v}}/\vec{y}\}\{\texttt{v}/x\} \int \texttt{l} ? (z:\texttt{bool}) . R \longrightarrow^* \\ R'_1 \int P\{\vec{\texttt{v}}/\vec{y}\}\{\texttt{v}/x\} \int R\{\texttt{v}/z\} \longrightarrow^* \mathbf{0} \end{array}$ 

where  $v \in bool$  since we start from a typed process and  $R'_2 \longrightarrow^* 1?(z:bool).R$ . We get

$$\begin{aligned} R_1 & \int \mathscr{E}[\mathbf{r} ! \langle x \rangle . P] \\ & \int R_2 \longrightarrow^* R'_1 \\ & \int \mathbf{r} ! \langle v \rangle . P\{\vec{v}/\vec{y}\}\{v/x\} \\ & \int R'_1 \\ & \int \mathbf{r} ! \langle v \rangle . P\{\vec{v}/\vec{y}\}\{v/x\} \\ & \int \mathbf{1} ? (z: bool) . R \longrightarrow R'_1 \\ & \int P\{\vec{v}/\vec{y}\}\{v/x\} \\ & \int R\{v/z\} \longrightarrow^* \mathbf{0} \end{aligned}$$

Vice versa, if  $R_1 \parallel \mathscr{E}[\mathbf{r} \mid \langle x \rangle . P] \parallel R_2$  is correct, then each reduction from  $R_1 \parallel \mathscr{E}[\mathbf{r} \mid \langle x \rangle . P] \parallel R_2$  to **0** must be of the shape shown above with  $\mathbf{v} \in \mathbf{bool}$ , and the proof is similar.

**Theorem 3.**  $A_i \parallel B_i \rightsquigarrow^* \operatorname{id}_{T_i}$  and  $B_i \parallel A_i \rightsquigarrow^* \operatorname{id}_{S_i}$  for  $1 \le i \le 12$ .

$$\begin{array}{l} \textit{Proof. The proof is by cases on i. For example \\ A_1 || B_1 & \stackrel{\rightarrow^+}{\to} 1?(x:t).1?(y:s).(x!(y), x!(y), id_T || B_1) \\ & \stackrel{\rightarrow^+}{\to} 1?(x:t).1?(y:s).(x!(y), x!(y), id_T || id_T) & \stackrel{\rightarrow^+}{\to} id_{tr.!s.T} \\ A_2 || B_2 & \stackrel{\rightarrow^+}{\to} r?(x:t).r?(y:s).(A_2 || 1!(y).1!(x), id_T) \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).r?(y:s).(1!(x).1!(y), id_T || id_T) & \stackrel{\rightarrow^+}{\to} id_{tr.!s.T} \\ A_3 || B_3 & \stackrel{\rightarrow^+}{\to} 1?(x:t).1 \triangleright \{r < inl..r!(x), id_T || fd_T \} & \stackrel{\rightarrow^+}{\to} id_{tr.!s.T} \\ A_3 || B_3 & \stackrel{\rightarrow^+}{\to} 1?(x:t).1 \triangleright \{r < inl..r!(x), id_T || fd_T \} & \stackrel{\rightarrow^+}{\to} r:r!(x), id_S || B_3 \} \\ & \stackrel{\rightarrow^+}{\to} 1?(x:t).1 \triangleright \{r < inl..r!(x), id_T || fd_T ) & \stackrel{\rightarrow^+}{\to} id_{tr.!s.T} \\ A_3 || B_4 & \stackrel{\rightarrow^+}{\to} r?(x:t).x!(x).1 \triangleright \{r < inl..d_T, r < inr.id_S \} & = id_{tr.(T \oplus S)} \\ A_4 || B_4 & \stackrel{\rightarrow^+}{\to} r?(x:t).A_4 || r \triangleright \{1 < inl.!d_T, 1 < inr.id_S \} & = id_{tr.(T \oplus S)} \\ A_4 || B_4 & \stackrel{\rightarrow^+}{\to} r?(x:t).x \triangleright \{1 < inl..id_T || id_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).x \triangleright \{1 < inl..id_T || id_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).x \triangleright \{1 < inl..id_T || id_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).1!(x).x \triangleright \{1 < inl..id_T || id_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).1!(x).x \triangleright \{1 < inl..id_T || id_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).1!(x).x \triangleright \{1 < inl..id_T || id_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).1!(x).x \triangleright \{1 < inl..id_T || d_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).1!(x).x \triangleright \{1 < inl..id_T || d_T, 1 < inr.id_S || id_S \} \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).1!(x).id_T = id_{1:ni..T} \\ & A_6 || B_6 & \stackrel{\rightarrow^+}{\to} r?(x:t).0!(id_T || id_T) \stackrel{\rightarrow^+}{\to} id_{1:ni..T} \\ & A_7 || B_7 & \stackrel{\rightarrow^+}{\to} 1?(x:t).0!(if x then r!(true).id_T = les (r < inr.id_T || B_7) \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).0!(if x then r!(true).id_T = les (id_T || r!(false).id_T) \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).0!(if x then r!(false).id_T \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).0!(if x then r!(true).id_T = les (A_8 || 1 < inr.id_T)) \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).0!(if x then r!(true).id_T = les (A_8 || 1 < inr.id_T)) \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).0!(if x then r!(false).id_T \\ & \stackrel{\rightarrow^+}{\to} r?(x:t).0!(if x then r!(false)$$

Point 3 is a straightforward consequence of Theorems 2 and 3.

## 4 Concluding remarks

Type isomorphisms have been mainly studied for various  $\lambda$ -calculi [10]. Pérez et al. [14] interpret intuitionistic linear logic propositions as session types for concurrent processes, which communicate only channels. So both their types and their processes differ from ours. In this scenario they explain how type isomorphisms resulting from linear logic equivalences are realized by coercions between interface types of session-based concurrent systems.

The notion of isomorphism for session types investigated in this paper can be used for automatically adapting behaviors, when their differences do not entail any loss of information. Adaptation in general [3] is much more permissive than in our approach, where we require adapters to be invertible. Moreover we only adapt processes as in [2, 11], while other works like [1, 8, 7] deal with adaptation of whole choreographies. Our approach shares many similarities with [5, 13] where *contracts* (as opposed to session types) describe the behavior of clients and Web services and filters/orchestrators mediate their interaction. The theory of orchestrators in [13] allows not only permutations of subsequent inputs and subsequent outputs, but also permutations between inputs and outputs if these have no causal dependencies. The induced morphism is therefore coarser than our isomorphism, but it may entail some loss of information.

There are some open problems left for future research. The obvious ones are whether and how our theory extends to recursive and higher-order session types. Also, we do not know yet whether the set of axioms in Table 3 is *complete*. The point is that in the case of arrow, product and sum types or of arrow, intersection, union types, it is known that the set of isomorphisms is not finitely axiomatizable [12, 9, 6]. Despite the fact that session types incorporate constructs that closely resemble product and sum types, it may be the case that the particular structure of the type language allows for a finite axiomatization. A natural question is to what extend our results are a consequence of the presence of just two channels in the process language, or whether they would carry over to calculi with arbitrary channel names. A more interesting research direction is to consider this notion of session type isomorphism in relation to the work on session types and linear logic [4, 15].

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