

# Formal Reasoning Using an Iterative Approach with an Integrated Web IDE

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This paper summarizes our experience in communicating the elements of reasoning about correctness, and the central role of formal specifications in reasoning about modular, component-based software using a language and an integrated Web IDE designed for the purpose. Our experience in using such an IDE, supported by a ‘push-button’ verifying compiler in a classroom setting, reveals the highly iterative process learners use to arrive at suitably specified, automatically provable code. We explain how the IDE facilitates reasoning at each step of this process by providing human readable verification conditions (VCs) and feedback from an integrated prover that clearly indicates unprovable VCs to help identify obstacles to completing proofs. The paper discusses the IDE’s usage in verified software development using several examples drawn from actual classroom lectures and student assignments to illustrate principles of design-by-contract and the iterative process of creating and subsequently refining assertions, such as loop invariants in object-based code.

## 1 Introduction

An IDE equipped with a formal verification system at its back end can facilitate an alternative style of developing software that involves using feedback from the verifier to locate and correct errors statically, instead of a more classical testing and debugging approach. This paper illustrates how such an approach can work in practice based on our experience in employing it to teach a software engineering course, where students are asked to develop software components that are provably correct with respect to a set of given specifications.

The discussion in this paper is in the context of teaching analytical reasoning to undergraduate CS students. The overall details of our educational approach for teaching mathematical reasoning, including an evaluation of student learning over multiple years in two required courses at Clemson, may be found in [11, 12]. Details of the types of software component development and reasoning assignments given to students are the topic of [7]. The purpose of the present paper is to explain the iterative approach that students and software engineers, in general, can employ for developing verifiably correct software using the feedback from the Web IDE and its integrated prover.

The IDE discussed in this paper is web-integrated, easy to use, and freely available online. It has been used at multiple institutions over the span of several years for teaching [9] and research [36] purposes, and is designed for RESOLVE, an integrated specification and programming language supported by a push-button verifying compiler [31]. The characteristics that distinguish the RESOLVE language and approach from most others include the following [21]:

- Mathematical theories used in specifying programming concepts are extensible and they are described in reusable mathematical units; The theories are purely mathematical and do not involve

any computational considerations. They are carefully engineered to ease automated proving. Number theory and a theory of strings over arbitrary types (used in this paper) are some examples.

- Specifications of programming concepts that encapsulate abstract data types are kept strictly separate from implementations to facilitate design-by-contract [26] and to allow for multiple ways of realizing the same concept and permit efficiency trade-offs. Examples of such concepts include programming integers with computational bounds, arrays, stacks, queues, and lists.
- The notion of clean semantics [21] makes it inessential to introduce and reason about object references explicitly in typical user code.

While the literature includes several integrated development environments based on formal techniques related to ours (see section 6 for a complete description), the one closest in spirit to the IDE discussed in this paper is the online Dafny IDE [24]. For most of the exercises discussed in this paper, Dafny could be used as well. However, the key difference that manifests itself the most for the purposes of this paper is our system’s usage of a VC generation system [15] that underlies the integrated Web IDE. Using the VCs and a supporting prover capable of revealing which VCs fail to prove, it is possible to determine why a proof was unsuccessful from givens in the context. However, unlike the Dafny approach, which is backed by Z3 [23], the IDE presented here cannot be used to provide counter examples when verification fails. The integrated prover does not use the proof-by-refutation technique, thus requiring a different sort of debugging to take its place. For example, a user contrasts what goal needs to be proved from the givens, tries to understand which givens would be more useful in attempting to prove the goal, and then adjusts the code or assertions as needed.

The reasoning process using the RESOLVE Web IDE is quite similar to what might be employed by one using a Coq-style proof assistant, except that the proofs to be done are mostly ‘obvious’ due to the simple nature of VCs arising from well-designed software [19]. This characteristic has allowed us thus far to forego the need for manual proof assistance for VCs.<sup>1</sup>

To illustrate how the IDE helps produce correct code based on realtime feedback, we begin with a simple example that involves only the use of the `Integer` datatype. This is followed by two object-based erroneous code examples: one that is recursive, and another that is iterative. These are examples presented to students as part of a software engineering course at Clemson. In all cases, we follow an iterative approach that eventually leads to the correct code or adequate annotations. The discussion concludes with a non-trivial queue copy example code with invariants that students were asked to develop for an assignment using the iterative approach. We note that the examples discussed in this paper are meant to give an idea of the iterative process. Several more complex components are available at the Web IDE; even more can be created by logging in to the site. We conclude the paper with a discussion of the most related work and a summary.

## 2 Understanding Design by Contract Using the IDE

In this and following sections, we provide several illustrative examples, each building in complexity, that demonstrate the iterative process we envision when using the Web IDE to develop provably correct code. All examples discussed have a shared emphasis on the debugging aspect: that is, each requires

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<sup>1</sup>A proof assistant such as Coq or Isabelle [29] is indeed necessary for proving the results in reusable mathematical units employed by the automated prover, but the focus of this paper is on code correctness and VCs, assuming that the necessary theorems have been established previously [20].

sufficient knowledge of design by contract to correctly identify and amend a variety of errors in code or annotations based on interactive feedback from the prover in the form of VCs.

The first, relatively simple example presents an operation that arithmetically swaps the values of two `Integer` objects. Taking advantage of the conceptual simplicity of the code comprising this initial example, we also use this as an opportunity to familiarize readers with RESOLVE style specifications, syntax, and layout of the Web IDE. More details on the design of the RESOLVE language and its IDE may be found elsewhere [9, 31, 32].

Upon opening our first example, `Int_Swap_Example_Fac` (Fig. 1), students are presented with code for a single operation, `Exchange`, that takes two `Integer` objects, denoted `I` and `J`. The essence of the specifications that formally communicate what exactly `Exchange` does can be found in the `ensures` clause (the postcondition), where we formally assert that  $I = \#J \wedge J = \#I$ . This assertion, when stated informally, can simply be read as “the outgoing value of `I` is equal to the incoming value of `J` and the outgoing value of `J` is equal to the incoming value of `I`.”<sup>2</sup> Notice also that there is no return value for the `Exchange` operation. Instead, we prefix each parameter with mode updates to indicate to clients that each of the `Integer` values passed will contain a purposeful value as specified by the conclusion of the operation.

```

1 Facility Int_Swap_Example_Fac;
2   uses Integer_Template;
3
4   Operation Exchange(updates I: Integer; updates J: Integer);
5     ensures I = #J and J = #I;
6   Procedure
7     I := I + J;
8     J := I - J;
9     I := I - J;
10  end Exchange;
11
12 end Int_Swap_Example_Fac;

```

Figure 1: Exchange operation with missing requires clause.

Software developers are free to edit both the specifications (formal contracts) of `Exchange`, as well as its executable body (sandwiched between the `Procedure` and `end` keywords). When ready to verify the operation, students may invoke an integrated prover. The exact prover used is of less importance to the discussion in this paper. It’s worth noting here, however, that our system is supportive of three approaches: one based on term-rewriting (accessible via the `RWVerify` button) [34], another that is currently under development and uses a congruence closure algorithm in conjunction with a matcher for quantified expressions (accessible via `CCVerify` button), and (optionally) an external SMT solver.<sup>3</sup> The second one that is designed to be just sufficient to prove VCs arising from program correctness (as opposed to arbitrary mathematical assertions) is summarized in section 5, and that is the one used for the examples in this paper.

Upon attempting to verify the `Exchange` operation, students are presented with a screen summarizing proof results, as shown in Fig. 2. The system generates eight distinct VCs [15]. VCs are mathematical

<sup>2</sup>In RESOLVE `ensures` clauses, `#` denotes the *incoming* value of a formal parameter.

<sup>3</sup>Z3 [27] is currently being incorporated as a proving option.

assertions that are both necessary and sufficient for the code to be proven correct. To understand why there are eight VCs, we briefly describe the design-by-contract idea in this setting. Two VCs arise from the two conjuncts of the `ensures` clause of the `Exchange` operation, guarantees to be provided by the implementer of the code. Six VCs, two each for the `requires` clauses (preconditions) of each of the three statements in the code, namely that the sum or differences do not go outside computational integer bounds (i.e., `min_int` and `max_int`), for a total of eight VCs. This is because preconditions of called operations are the responsibility of the calling code in design-by-contract. Placing the cursor near the line number of a statement causes a box to appear referring to the names of one or more VCs generated if the statement produces VCs.

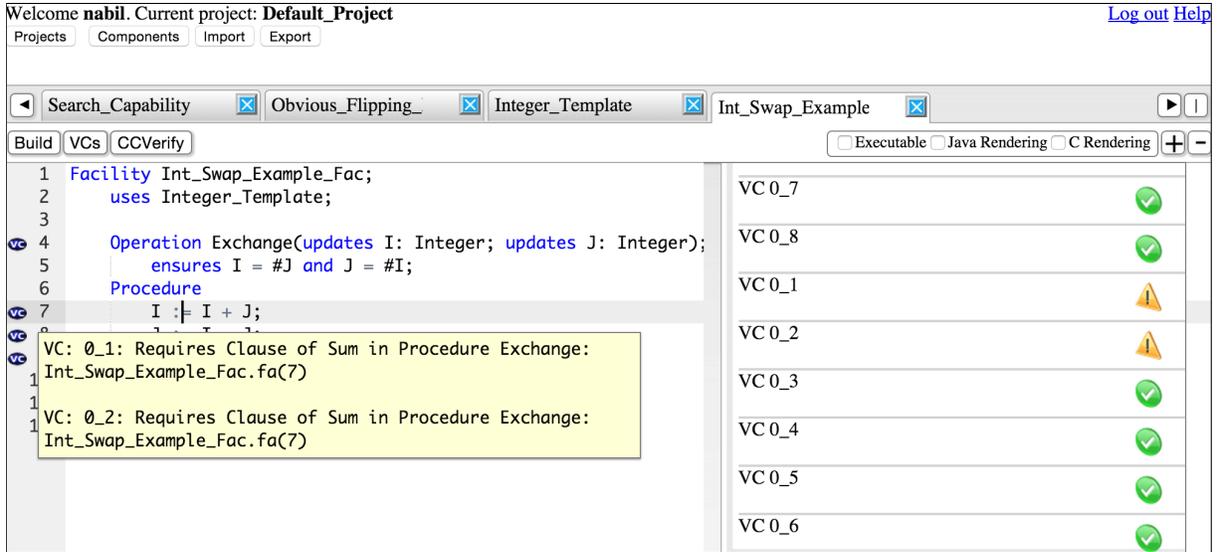


Figure 2: Proof attempt of `Exchange` operation with missing `requires` clause.

Of the eight VCs, two are unable to be proven, as indicated by the yellow exclamation marks beside `VC_01` and `VC_02` (Fig. 3). The line numbers in code corresponding to the VC are given in parentheses. While VCs in general might arise from any number of sources within a block of executable code, those unable to be established here arise from the `requires` clause of the sum operation that is implicitly invoked when `I` and `J` are added via the `+` operator. We leave it to a reader to convince themselves that an overflow or an underflow can occur in this code only for the first statement.

To aid students in arriving at the particular insight necessary to debug this code, we encourage them to interactively explore the unprovable VCs by mousing over context sensitive VC buttons appearing next to lines of code that generated VCs. Upon clicking any of these buttons, the panel on the right hand side of the Web IDE updates with relevant, finer grained information about the particular VC queried, including a succinct description of what must be established (the goal) and the various facts (givens) the system currently knows.<sup>4</sup>

In terms of the `Exchange` example, it is easy to observe that the system is unable to infer from the givens that  $\text{min\_int} \leq (I + J)$  (`VC 0_1`) and  $(I + J) \leq \text{max\_int}$  (`VC 0_2`). It then becomes possible to infer that the system currently lacks knowledge suitable to realize that `Integer` overflow (or underflow) will not occur when the `+` operation is carried out. To remedy this, and ‘provide’ the

<sup>4</sup>A parsimonious approach to the generation of givens is under research and several of the unrelated givens are expected to disappear in the next version of the IDE.

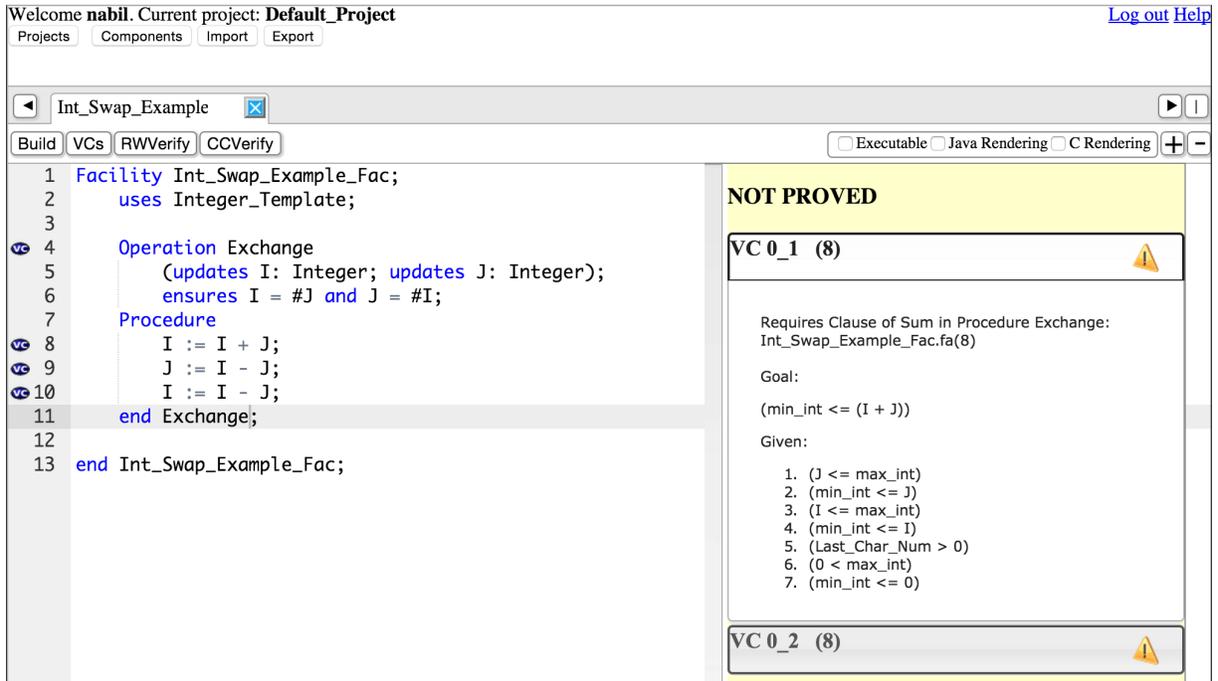


Figure 3: Full display of a VC in the IDE.

system with the assurance that this will not happen, students must defer to their knowledge of design-by-contract, amending the specification of `Exchange` with a suitable requires clauses as shown in Fig. 4. Again, under design-by-contract, the requirements become givens to be used in proofs. The figure shows that the Web IDE successfully verifies the code using the improved operation specification.

### 3 Debugging Recursive Code

For the second example, we consider an example operation which inverts the order of items in a queue (see Fig. 5). The `Invert` operation is specified in an enhancement (an extension using specification inheritance) to the `Preemptable_Queue` concept and implemented in an enhancement realization, using only operations provided in the `Preemptable_Queue` concept. This separation of concerns makes it possible to verify the enhancement realization in a modular fashion without referring to or refining to any one implementation of `Preemptable_Queue` concept. A preemptable queue differs from a regular queue in that it has operations to “inject” new items at the front of the queue (i.e., preempt the regular queue order), in addition to regular queue operations, such as `Enqueue` and `Dequeue`.

A complete specification of the `Preemptable_Queue` concept is shown in Appendix A. In the `Preemptable_Queue` concept, the contents of the queue are conceptualized as a mathematical string (a structure similar to but simpler than a sequence in  $\mathbb{Z}$ , because no positions are involved). So for this operation, the ensures clause (or post-condition) states that the outgoing value of the parameter  $Q$  should be the mathematical reverse of the input parameter (denoted by  $\#Q$ ). Suppose that this operation is implemented using faulty code such as is shown in Fig. 6. Three of the VCs are verified, but VC 0\_3 is not. So as we did before, we encourage students to take a close look at that particular unprovable VC.

In the goal,  $E'$  is the dequeued entry,  $Q'$  is the conceptual string that stands for the value of the queue

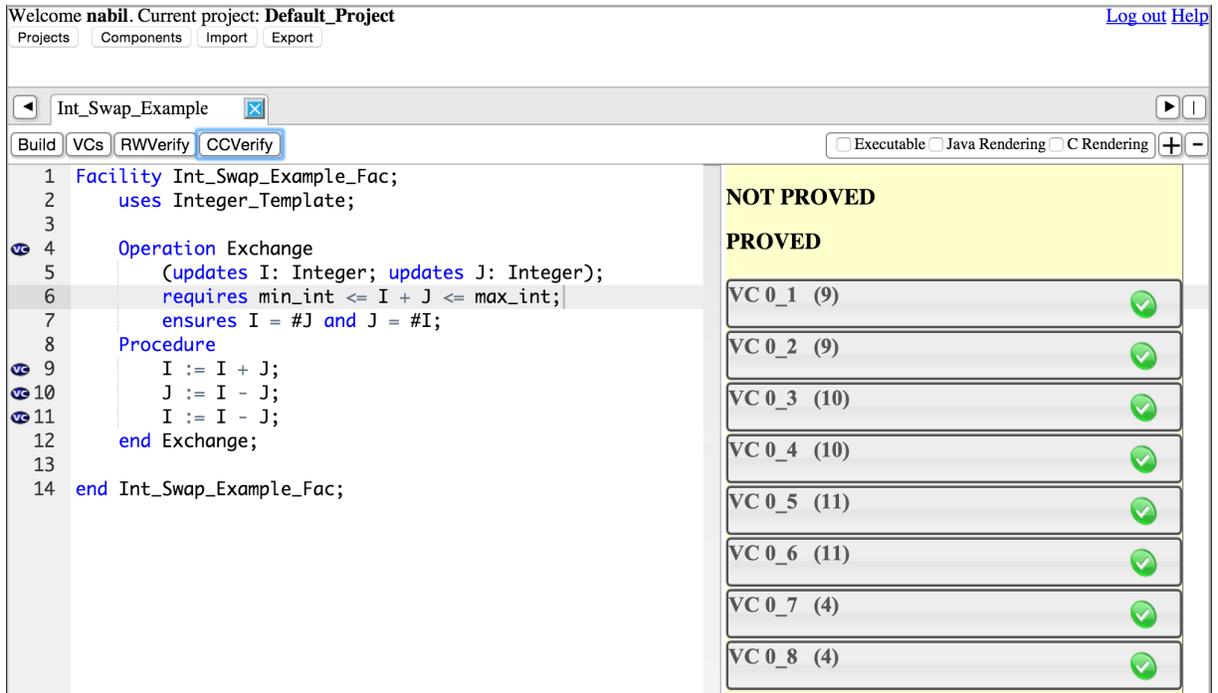


Figure 4: Int\_Swap\_Example\_Fac verified.

passed into the recursive call of `Invert`, and  $Q$  stands for the value of the queue at the beginning of the procedure. The goal is that  $E'$  concatenated with the reverse of  $Q'$  is equal to the reverse of  $Q$ . In order to debug this VC a user may first write down the goal and then apply transformations until we can clearly observe why the goal is unprovable. The purpose here is to show a general process when the problem with the unprovable VC is less obvious.<sup>5</sup>

Goal:  $Q' = \text{Reverse}(Q)$

Our first transformation will be to use given #1 and apply it to the left-hand side of the goal. We will label this transformation Step 1.

Step 1:  $\langle E' \rangle \circ Q'' = \text{Reverse}(Q)$

Next, we will apply given #2 to transform the left-hand side once again:

Step 2:  $\langle E' \rangle \circ \text{Reverse}(Q''') = \text{Reverse}(Q)$

And then we apply given #3 to the right-hand side:

Step 3:  $\langle E' \rangle \circ \text{Reverse}(Q''') = \text{Reverse}(\langle E' \rangle \circ Q''')$

Next, one can attempt to use a theorem from `String_Theory`, which defines string notations and results involving those notations for mathematical strings. The theorem we need here states the following:

<sup>5</sup>While we show the details of these steps here, in actual debugging, such a detailed analysis may not be necessary; understanding of such principles as string concatenation is not commutative is straightforward and the problem may be inferred more readily.

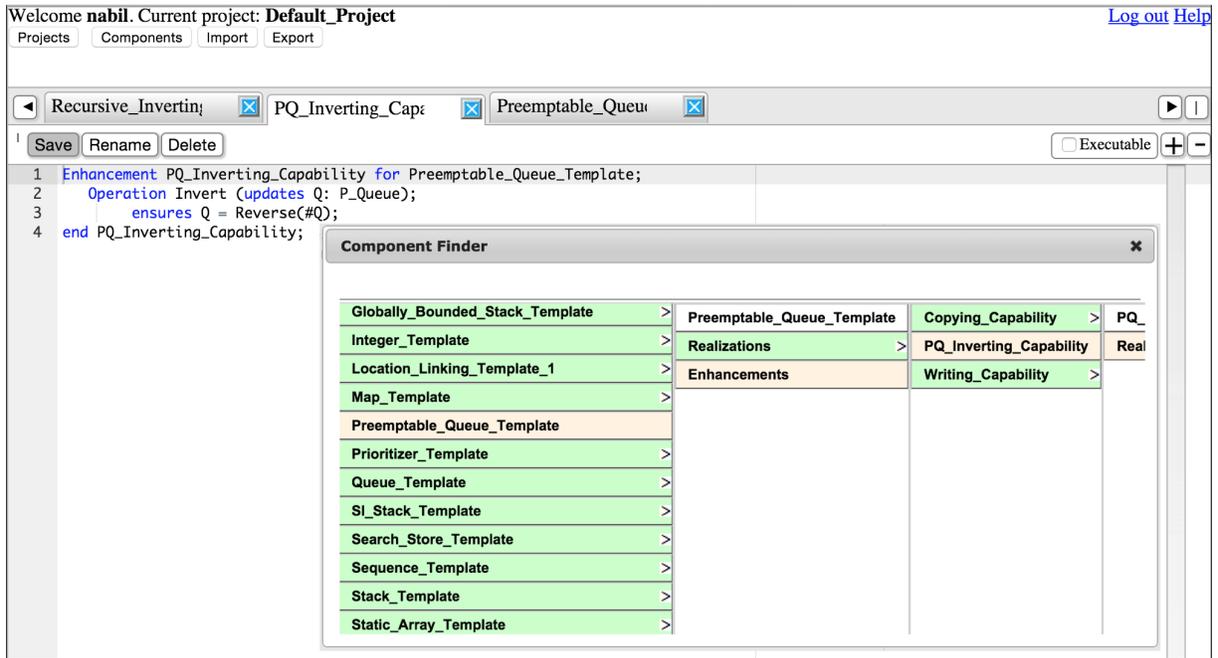


Figure 5: Selection of an enhancement in the Web IDE.

For all  $u, v : \text{String}$ ,  $\text{Reverse}(u \circ v) = \text{Reverse}(v) \circ \text{Reverse}(u)$

This transformation will produce the following result:

Step 4:  $\langle E' \rangle \circ \text{Reverse}(Q''') = \text{Reverse}(Q''') \circ \text{Reverse}(\langle E' \rangle)$

Finally, we apply a theorem that states that the reverse of a single-length string is itself, which gives us step 5:

Step 4:  $\langle E' \rangle \circ \text{Reverse}(Q''') = \text{Reverse}(Q''') \circ \langle E' \rangle$

At this point, it is obvious to see that the goal is categorically false, as the concatenation operator is not commutative. Thus, the problem with the code is that the call to `Inject` is placing the dequeued entry on the wrong side of the recursively inverted queue. This example illustrates how the VC can serve as a guide to pinpoint the source of the error in formal reasoning.

In the case, the correction is to fix the code: Specifically, the call to `Inject` needs to be replaced with a call to `Enqueue`.

## 4 Loop Invariants

This section outlines creation and iterative development of loop invariants for code using object-based examples. Stacks and queues are abstract data types represented as objects in RESOLVE. Their behavior is specified in a *Concept*, which is an abstract description of the methods all implementations must contain. It concludes with a discussion of an assignment given to students in a junior level software engineering course.

Welcome **nabil**. Current project: **Default\_Project** [Log out](#) [Help](#)

Projects Components Import Export

Recursive\_Invertin; [X] ▶ |

VCs RWVerify CCVerify | Save Rename Delete  Executable + -

```

1 Realization Recursive_Inverting_Realiz
2   for PQ_Inverting_Capability
3     of Preemptable_Queue_Template;
4     Recursive Procedure Invert(updates Q: P_Queue);
5       decreasing |Q|;
6
7       Var E: Entry;
8       Var Len: Integer;
9
10      Len := Length(Q);
11      If ( Len /= 0 ) then
12        Dequeue(E, Q);
13        Invert(Q);
14        Inject(E, Q);
15      end;
16    end Invert;
17  end Recursive_Inverting_Realiz;

```

**NOT PROVED**

VC 0\_3 (4) ⚠

Ensures Clause of Invert:  
Recursive\_Inverting\_Realiz.rb(4)

Goal:  
(Q' = Reverse(Q))

Givens:

1. (Q' = (<E'> o Q''))
2. (Q'' = Reverse(Q'''))
3. (Q = (<E'> o Q''))
4. (|Q| /= 0)
5. (P\_val = |Q|)
6. Entry.Is\_Initial(E)
7. (|Q| <= Max\_Length)
8. (Last\_Char\_Num > 0)
9. (0 < max\_int)
10. (min\_int <= 0)
11. (Max\_Length > 0)

Figure 6: Inverting Code with Error.

#### 4.1 Learning Iterative Invariant Development

We begin with a simple example involving stacks to highlight the iterative steps we commonly see students working through with our Web IDE in reasoning about, and ultimately arriving at, appropriate assertions for loop invariants. Stacks, like queues, are modeled mathematically using strings; operations such as Push and Pop are specified using string notations.

Fig. 7 shows an example operation presented in a classroom to teach the idea of invariants. Flip\_onto iteratively transfers entries from a source stack, S, to a destination stack, T, resulting in a version of T that is prefixed by a ‘flipped’ version of S. As expected, the intuition describing this outcome is formalized in the operation’s ensures clause by the following succinct assertion:  $T = \text{Reverse}(\#S) \circ \#T$ . With the operation’s input/output behavior formally expressed, students must turn to the task of deriving a suitable invariant for the while loop, expressed in RESOLVE using the maintaining clause. (The decreasing clause is used to document the progress metric necessary to prove termination.)

Starting with a maintaining clause that simply reads “true”—which is an appropriate starting point for beginners to understand the process of developing adequate invariants—the system (unsurprisingly) fails to establish correctness. Aside from the obvious inability to prove VCs corresponding to the operation’s overall ensures clause, students using the Web IDE are able to see—with the help of the interactive VC buttons next to the line numbers—that VC 1\_1 and 1\_2 arising from calls to Pop and Push within the body of the loop are currently unprovable, as shown in Fig. 7.

Examining VC 1\_1, students are immediately informed that the requires clause of Pop ( $|S'| \neq 0$ ) cannot be established. Referring to the list of available givens, students can see that while the system is aware that  $D' \neq 0$ , it still lacks any knowledge relating the length of S to the current depth, D. To address this roadblock and provide the prover with the information it needs to meet the precondition criteria of Pop, students might start by amending the maintaining clause with the assertion that  $|S| = D$ .

Welcome **nabil**. Current project: **Default\_Project** [Log out Help](#)

Projects Components Import Export

Stack\_Examples\_F: [x] [▶] [⏏]

Build VCs RWVerify CCVerify Save Rename Delete Executable Java Rendering C Rendering [+]

```

1 Facility Stack_Examples_Fac;
2   uses String_Theory;
3
4 Facility Rev_Stack_Fac is Stack_Template(Char_Str, 1)
5   realized by Array_Realiz;
6
7 Operation Flip_onto(clears S: Stack; updates T: Stack)
8   requires |S| + |T| <= 1;
9   ensures T = Reverse(#S) o #T;
10 Procedure
11   Var Next_Entry: Char_Str;
12   Var D: Integer;
13
14   D := Depth(S);
15   While (D /= 0)
16     maintaining true;
17     decreasing |S|;
18   do
19     Pop(Next_Entry, S);
20     Push(Next_Entry, T);
21     D := Depth(S);
22   end;
23 end Flip_onto;
24
25

```

**NOT PROVED**

VC 1\_1 (19) ⚠

Requires Clause of Pop in Procedure Flip\_onto:  
Stack\_Examples\_Fac.fa(19)

Goal:  
(|S'| /= 0)

Given:

1. (D' /= 0)
2. (P\_val' = |S''|)
3. true
4. (1 <= max\_int)
5. (min\_int <= 1)
6. (Max\_Depth = 1)
7. (|T| <= Max\_Depth)
8. (|S| <= Max\_Depth)
9. ((|S| + |T|) <= 1)
10. (Last\_Char\_Num > 0)
11. (0 < max\_int)
12. (min\_int <= 0)

VC 1\_2 (20) ⚠

Figure 7: Loop with insufficient invariant.

Sure enough, upon re-running the prover, students are given validation in the form of a green checkmark, indicating that one roadblock to verification of the current operation has been successfully dealt with.

In motivating further construction of the maintaining clause, students once again look to unproven VCs as a guide to development. In this case, looking specifically to VC 2\_2, students can see that the ensures clause to the overall operation is still unable to be established. Using this insight, combined with the goal this VC is attempting to establish—specifically, that  $T' = (\text{Reverse}(S) \circ T)$ —one way for students to proceed is to simply append this assertion to the evolving maintaining clause, yielding  $|S| = D$  and  $T = (\text{Reverse}(\#S) \circ \#T)$ . Upon doing so, students can indeed see that the prover is now able to establish the ensures clause of the operation (indicated by VC 2\_3), but is unable to establish the VC corresponding to the invariant of the while statement—suggesting that something is still lacking from the assertion. However, in examining the (now provable) goal of the overall ensures clause addressed in the previous step, students can see that it reads as follows:  $(\text{Reverse}(S) \circ T) = (\text{Reverse}(S) \circ T)$ . Thus, mirroring the same technique and intuition employed to make the ensures clause provable earlier—that is, adding  $T = (\text{Reverse}(\#S) \circ \#T)$ —students can now make the necessary cognitive leap to realize the clause must be changed to read:  $\text{Reverse}(S) \circ T = \text{Reverse}(\#S) \circ \#T$ , resulting in a final, provable assertion that reads:  $D = |S|$  and  $\text{Reverse}(S) \circ T = \text{Reverse}(\#S) \circ \#T$ .

## 4.2 Applying Iterative Invariant Development

Following an introduction to the iterative development of loop invariants and discussion, students used the Web IDE to complete reasoning assignments. The assignments required students to write verified

code for pre-specified concepts and enhancement operations. The specification of one such operation to copy a generic `Preemptable_Queue` is given below.

```
Operation Copy_Queue
  (restores Q: P_Queue; replaces Q_Copy: P_Queue);
  ensures Q_Copy = Q;
```

Table 1 is a summary of student performance for each of the invariant writing assignments. In addition to copying a queue, students wrote code for outputting a queue, reversing a sequence, and an end user application assignment that involved use of custom-made mathematical definitions and operations involving non-trivial types. The definitions in the end user assignment were not complemented by necessary results and hence, the prover was not of use in establishing the invariants. The complexity of the assignment, the mathematics involved, and the absence of prover support are among possible reasons for the low success rate of students in developing appropriate invariants for those operations.

	Writing (Queue)	Copying (Queue)	Reversal (Sequence)	Facility Operations
<b>Correct</b>	70%	90%	60%	30%
<b>Insufficient Invariant</b>	20%	10%	30%	
<b>Other</b>	10%		10%	70%

Table 1: Evaluation of Invariant Assignments

Fig. 8 is an example of code developed by a student for the `Copy_Queue` assignment.<sup>6</sup> Neither the code nor the invariant is necessarily optimal. Proofs of all 18 VCs generated for this copy operation are completed in an average time of 3 seconds (total) on the server that hosts the Web IDE. As noted earlier in the introduction, other provers, such as Z3, could be used to discharge the VCs.

## 5 Summary of the Prover Underlying the Web IDE

The verifying compiler that serves as the back end of the web interface contains a modular VC generation subsystem [8, 15] which provides input to an automated prover. The automated prover relies on previously proven results in a library of mathematical theories that are reused in the specification of programming concepts [34].

At the core of the *CCVerify* automated VC prover is a congruence closure algorithm that incorporates the Theory of Equality, similar in spirit to that described in [28]. An outer layer that incorporates pattern matching techniques for expressions containing universally quantified variables is engaged, similar to the matcher described in [10]. In this way, a single component can handle problems from multiple domains. *CCVerify*, which contains fewer than 2000 lines of Java code, is designed to be fast, simple, and effective. As it is fully integrated into the compiler, there are no issues with portability, licensing, or translation of the assumptions to other formats as there might be if an external tool were used.

There is an implied division of labor in the production of proofs. Specifications must be written so that the consequent of the VC eventually produced is a predicate with constants as arguments. It

<sup>6</sup>In the figure, `changing` clause is optional and it lists variables potentially affected by the loop; variables not mentioned are assumed to be unchanging. In the absence of this clause, all variables are assumed to be affected. This clause is useful to simplify some routine invariants [15].

Figure 8 shows a screenshot of a web IDE interface. The top bar displays 'Welcome nabil. Current project: Default\_Project' and navigation buttons for 'Projects', 'Components', 'Import', and 'Export'. Below this is a tabbed window with several open files, including 'Iterative\_Copying\_'. The main editor area shows the following code:

```

1  -- Component completed by David K.
2  Realization Iterative_Copying_Realiz
3  (
4      Operation Copy_Entry(restores E: Entry; replaces R: Entry);
5          ensures R = E;
6  )
7  for Copying_Capability of Preemptable_Queue_Template;
8  Procedure Copy_Queue(restores Q: P_Queue; replaces Q_Copy: P_Queue)
9      Var I, J: Integer;
10     Var Q_Length: Integer;
11     Var Temp1 : Entry;
12     Var Temp2: Entry;
13     Var TempQ: P_Queue;
14     Clear(Q_Copy);
15     I := Length(Q);
16     While I > 0
17         changing Temp1, Temp2, TempQ, I, Q, Q_Copy;
18         maintaining #Q = Q_Copy o Q and I <= |#Q| and |Q| = I
19             and TempQ = Q_Copy;
20         decreasing I;
21     do
22         Dequeue(Temp1, Q);
23         Copy_Entry(Temp1, Temp2);
24         Enqueue(Temp1, TempQ);
25         Enqueue(Temp2, Q_Copy);
26         I := Length(Q);
27     end;
28     TempQ := Q;
29 end Copy_Queue;
30 end Iterative_Copying_Realiz;

```

On the right side of the IDE, a list of 13 verification conditions (VCs) is displayed, each with a green checkmark indicating it is satisfied:

- VC 0\_5 (22)
- VC 0\_6 (24)
- VC 0\_7 (25)
- VC 0\_8 (19)
- VC 0\_9 (19)
- VC 0\_10 (19)
- VC 0\_11 (19)
- VC 0\_12 (20)
- VC 1\_1 (19)
- VC 1\_2 (19)
- VC 1\_3 (19)

Figure 8: Student code for the Queue copy assignment.

turns out that typically it is sufficient to use only instances of previously proven universally quantified statements (these are members of a reusable theorem library) to construct a proof, assuming that the specifications used to generate the VC make such a proof relatively obvious. Sophisticated techniques used in automated theorem provers may not be required to discharge the VCs. We are currently testing this hypothesis using a limited version of Z3 as well.

Our mathematical specification system is feature-rich. It allows for polymorphic types and first class functions. These features make a *direct* translation of *some* of our mathematical theories to the standard many-sorted first-order logic language [2] used in SMT proving impossible, though it is possible to support these features relatively simple (in a sound, but not complete way) within the matching component of the integrated prover.

## 6 Related Work

A summary of related specification/verification languages may be found in [16], and tools or IDEs to facilitate their usage are discussed in the first proceedings of this workshop [13]. We discuss only the most related work in this section.

Like RESOLVE that underlies the Web IDE discussed in this paper, Dafny is a programming and specification language intended for verification of functional correctness [24]. The Eiffel integrated language effort [25], though initially focused on runtime assertion checking, is now supported by Eve [14], the Eiffel Verification Environment, which includes the AutoProof [35] tool for static verification. Both Dafny and AutoProof translate code and specifications into Boogie [1], an intermediate verification lan-

guage. The Boogie tool can create VCs suitable as input to an SMT solver. An important distinction is that the RESOLVE compiler handles VC generation internally, and displays them in a format that makes it easy for users to connect problematic VCs with the code and specifications that produced them.

Java Modeling Language (JML) is a specification/verification language for Java programs, and tools for the language include the ability to do runtime assertion checking [22]. JML does not have an IDE, but there are efforts to integrate JML as plugin to Eclipse [4, 5]. Tools are available for ProB, an animation and a model checker for the B-Method, which is a formal method based on abstract machine notation [3, 37]. The VeriFast effort is aimed at verifying single/multi-threaded C and Java programs [17, 33]. VeriFast also includes a GUI that is packaged into their code distribution.

We are not alone in employing a formal methods IDE in education. Whereas our educational focus is mostly on software engineering aspects (though we have used the Web IDE to a limited extent in a discrete mathematics course), teaching discrete mathematics and specifications using an IDE is the focus of FoCaLiZe—an IDE that takes source code, specification properties, and machine-checkable proofs to produce executable OCaml code and checkable Coq input values [18, 30]. Though our Web IDE does not support inferring loop invariants, invariant inference is a useful feature; an Eclipse plugin with a goal to infer object and loop invariants for C programs is discussed in [6].

## 7 Conclusions

This paper has detailed an iterative approach for creating, debugging, and developing components that are correct with respect to their specifications, using an IDE equipped with a verification system. Using several illustrative examples drawn from lectures and student assignments, we have explained how students and software engineers, in general, can develop provably correct software iteratively based on the VCs and feedback received from the RESOLVE Web IDE. Extensive experience with the IDE in the classroom indicates that students are indeed capable of producing correct software using the the IDE as discussed in this paper. While the present paper has focused only on functional correctness of code, the IDE includes features to create and view mathematical units and data abstraction realizations with representation invariants and abstraction relations, as well as for generating executable Java code from RESOLVE code [36].

A variety of improvements to the IDE are in progress, ranging from minor visual improvements, such as highlighting VC buttons that correspond to unprovable VCs, to more significant ones, such as the creation and development of performance specifications and related correctness checks.

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## A Preemptable\_Queue Specification

```

Welcome nabil. Current project: Default_Project Log out Help
Projects Components Import Export

Preemptable_Queueu [x] [▶] [⏏]
 Executable + -

1 Concept Preemptable_Queue_Template(type Entry; evaluates Max_Length: Integer);
2   uses String_Theory;
3   requires Max_Length > 0;
4
5   Type Family P_Queue is modeled by Str(Entry);
6     exemplar Q;
7     constraint |Q| <= Max_Length;
8     initialization ensures Q = Empty_String;
9   end;
10
11 Operation Enqueue(alternes E: Entry; updates Q: P_Queue);
12   requires |Q| < Max_Length;
13   ensures Q = #Q o <#E>;
14
15 Operation Inject(alternes E: Entry; updates Q: P_Queue);
16   requires |Q| < Max_Length;
17   ensures Q = <#E> o #Q;
18
19 Operation Dequeue(replaces R: Entry; updates Q: P_Queue);
20   requires |Q| /= 0;
21   ensures #Q = <R> o Q;
22

```

**Updates**  
The value passed in will have a purposeful change at the end of the operation.

Figure 9: Partial Preemptable\_Queue specification.