COMP4211 Adv. Architectures



Introduction

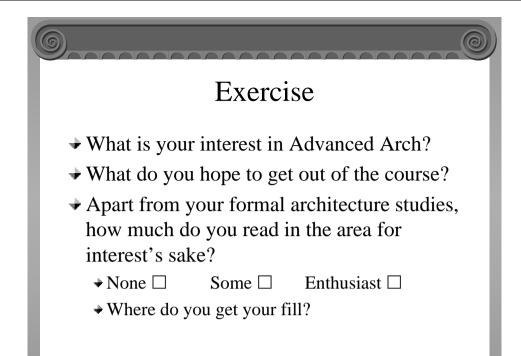
- → What are we going to study?
 - → Advanced pipelining
 - **→**H&P App A., Ch 3 & 4
 - → Reconfigurable microprocessors
 - →Research papers
- → How will we study it?
 - **→** Seminars

Introduction

- → What else will you need to do?
 - → Project
 - **→**Proposal
 - **→**Presentation
 - **+**Report
 - **→** Seminar log

Introduction

- → Assessment
 - → Seminar presentation 30/100
 - → Presentation quality
 - → Thoroughness
 - → Ability to answer questions
 - → Project 60/100
 - → Proposal 5/60
 - → Presentation 15/60
 - → Report 40/60
 - → Seminar log/class participation 10/100



Exercise

- → Define
 - **→** Pipelining
 - → In-order execution
 - **→** Out-of-order execution
 - **→** Static scheduling
 - → Dynamic scheduling
 - **→** Superscalar
 - → Reconfigurable computing